Multimoded RF Systems for Future Linear Colliders

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Acknowledgment

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- K. Ratcliffe and the Vacuum Assembly Team.
- We wish to Thank all operators that took shifts 24 hours a day for several months

Outline

- Dualmode resonant delay line pulse compression system for the Next Linear Collider (NLC)
 - 1. Introduction
 - 2. Components: design and cold tests
 - 3. Dualmode Delay Lines: Design and Experimental results
 - 4. High power experimental results

RF Accelerator Structures needs a short, high power, rf pulse. For example the current NLC design requires a flat-top 396 ns rf pulse with a power level of about 95 MW/m at 11.424 GHz, and a repetition rate of 120 Hz to feed each accelerator structure.

One need to transfer the CW wall plug power to rf pulses with high power and low duty factor. Hence

- 1. A storage system is needed.
- 2. A Switch or a switching mechanism is needed to control the charging and discharging of the system.
- 3. A device to generate the RF power

Storage Systems:

1. Capacitors; i.e. Modulators.

Switches include pulse forming networks, thyrotrons, IGBTs, and grided guns on microwave tubes.

2. Kinetic energy of an electron beam ; i.e. two beam accelerator,

Switches include RF beam kickers

- 3. In the accelerator structure; i.e. super conducting accelerators.
- 4. In rf transmission lines and cavities; i.e. rf pulse compression systems.

Switches includes rf phase manipulation between rf sources, and solid state switches.

In general most of rf systems, suggested for a linear collider, contains elements from several of the above storage system.

To compare these system one has to consider:

- 1- Philosophy of the design
 - Modularity: one may choose to have a unit that contains one rf source, and compact pulse compression system such as SLED-II
 - Flexibility in the operating rf frequency: one may choose Two beam systems
 - •....
- 2- Efficiency
- 3- Cost

The choice of the system is greatly affected by the available technology of the components.

If one has

1- A really inexpensive efficient rf source

2- A a very efficient and inexpensive modulator system using a very fast switch (such as grided gun on the rf source)

One might use several thousands of these devices to power the main Linac of a collider

However, neither the *inexpensive* rf source nor the *very fast* modulator exist, only ideas at the moment.

The need for RF Pulse compression

1- It is usually easier to build rf sources with low power and long pulse width.

2- Rf sources are expensive, one should get as much energy from them as possible, i.e., the longest possible pulse. One should not let an expensive rf source in an idle mode most of the time.

Hence, rf pulse compression is needed to match the long pulse low power of available rf sources such as klystrons to the high power short pulse needed for the accelerator structure.

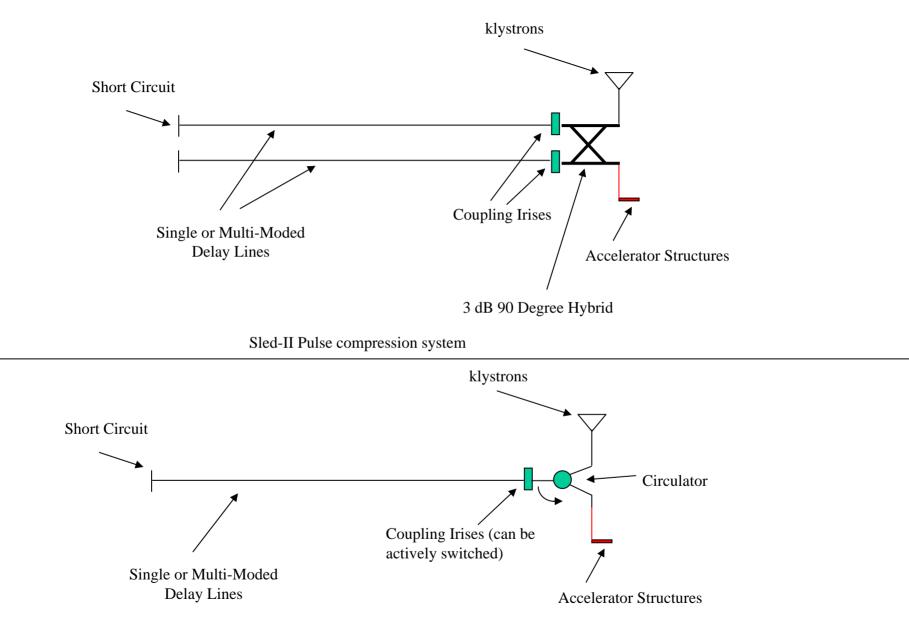
• Pulse Compression should be used with as high of a compression ratio as possible until

- a) The cost of the compression system starts to exceed that of the klystrons and modulator
- b) Or, the available pulse width of the klystrons is exhausted
- The cost of the main linac is directly proportional to the efficiency of the pulse compression system.

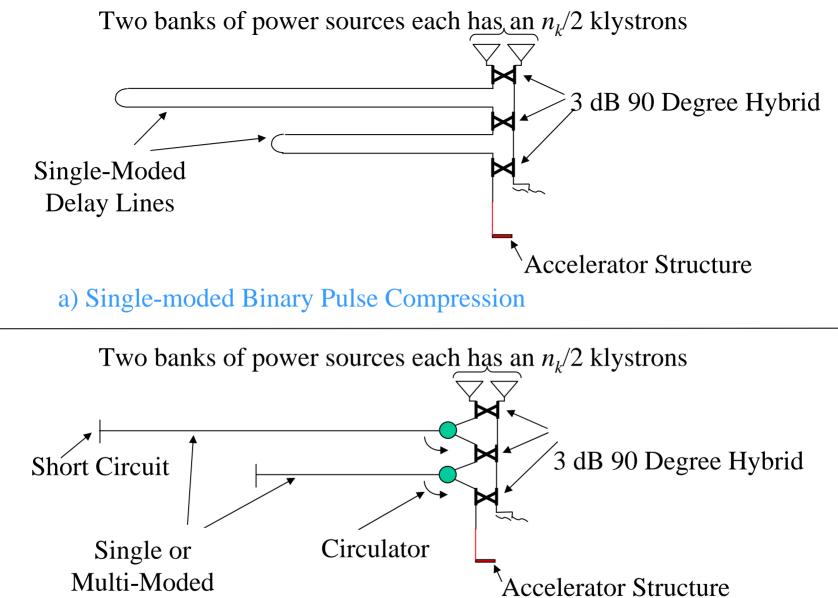
RF Pulse compression for RF Linacs and Colliders

- Resonance Delay Lines (SLED-II)
- Binary Pulse Compression (BPC)
- Delay Line Distribution System (DLDS)

• Two-Stage systems, any combination of two or more of the above systems

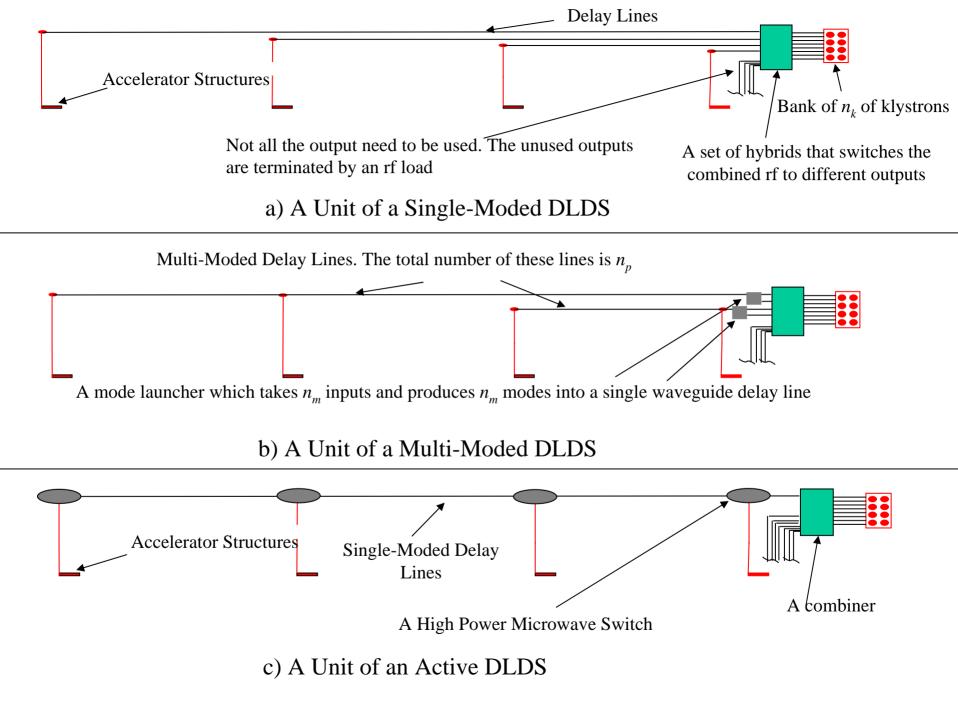


Sled-II pulse compression system with a circulator and active switches



Delay Lines

b) Binary pulse compression can have several improvements including the use of a circulator and several modes to reduce the delay line length.



The challenges facing most of these pulse compression system are

1- Compactness, how to produce a storage system which is relatively compact

2- Efficiency, for the resonant delay lines, efficiency could be boosted by an rf switch.

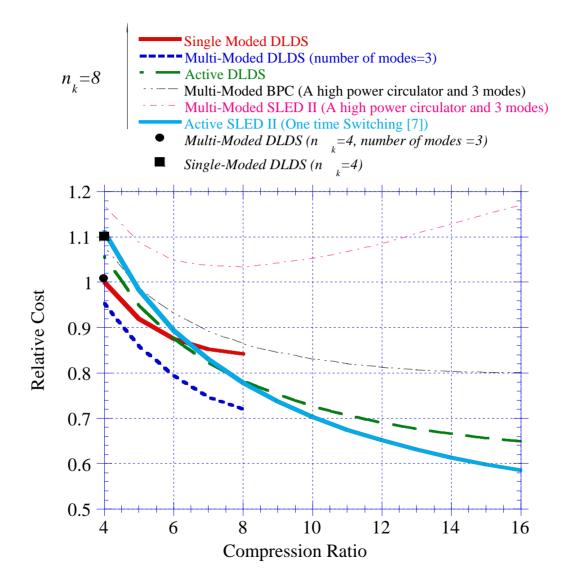
3- Most of these systems could have a more compact topology if one have, a nonreciprocal RF device (circulator), or a switch.

Compactness

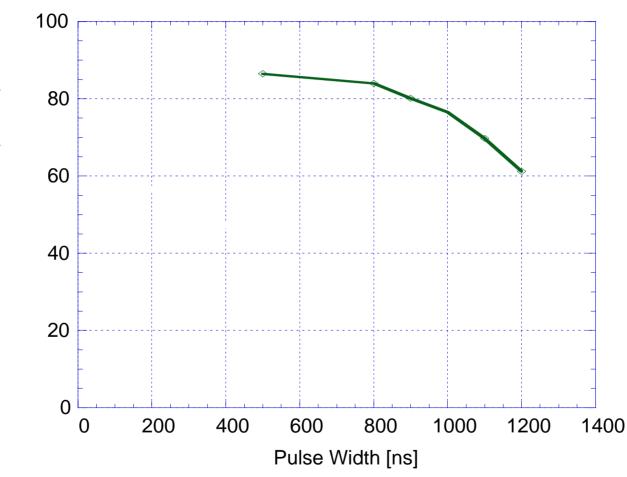
•A waveguide near cutoff, hence, a low group velocity. One can use a higher order mode to reduce the losses. A bad idea, dispersion will destroy the pulse shape at a group velocity less than 4.

•Loaded waveguide (slow wave structure). A bad idea because of dispersion and losses.

•Multimoded Waveguide, the only good idea with no draw backs. We are using highly overmoded waveguide systems no mater what, using an extra mode, or two, or four, .. Is a bonus.



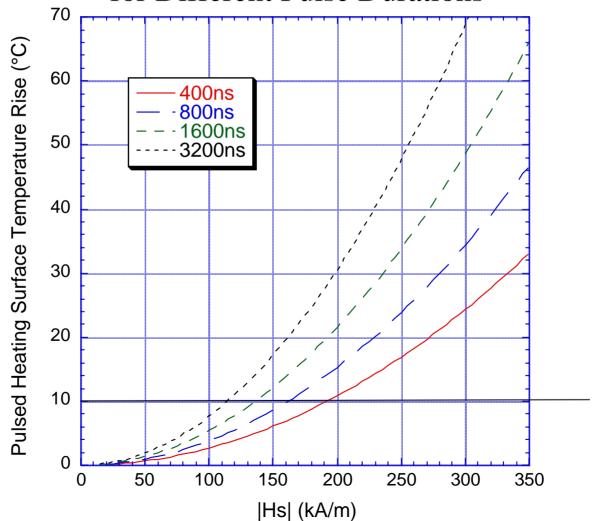


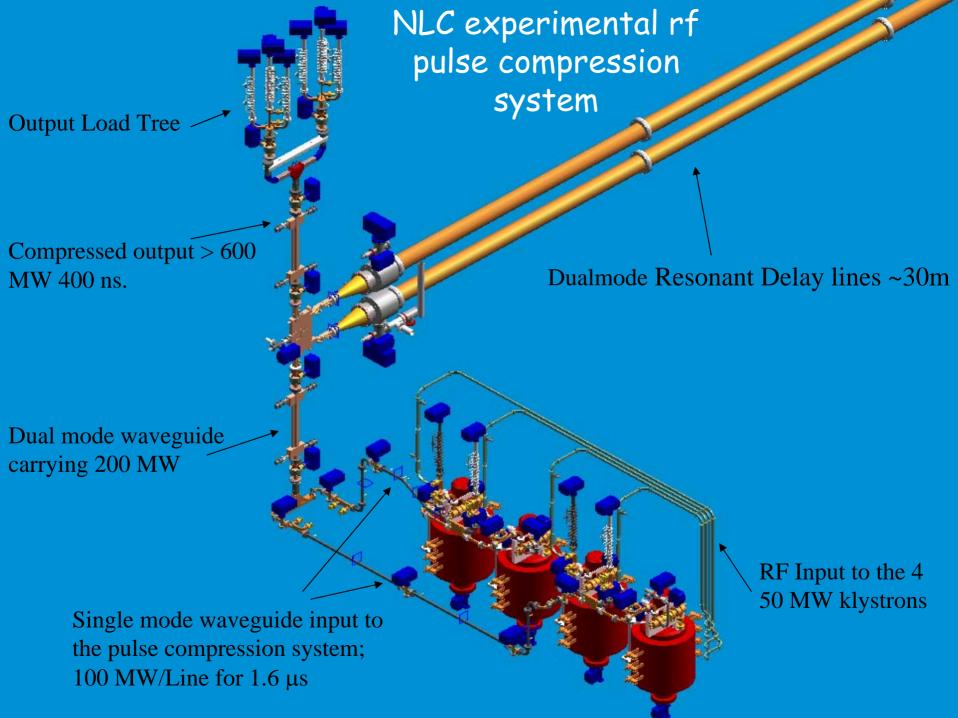


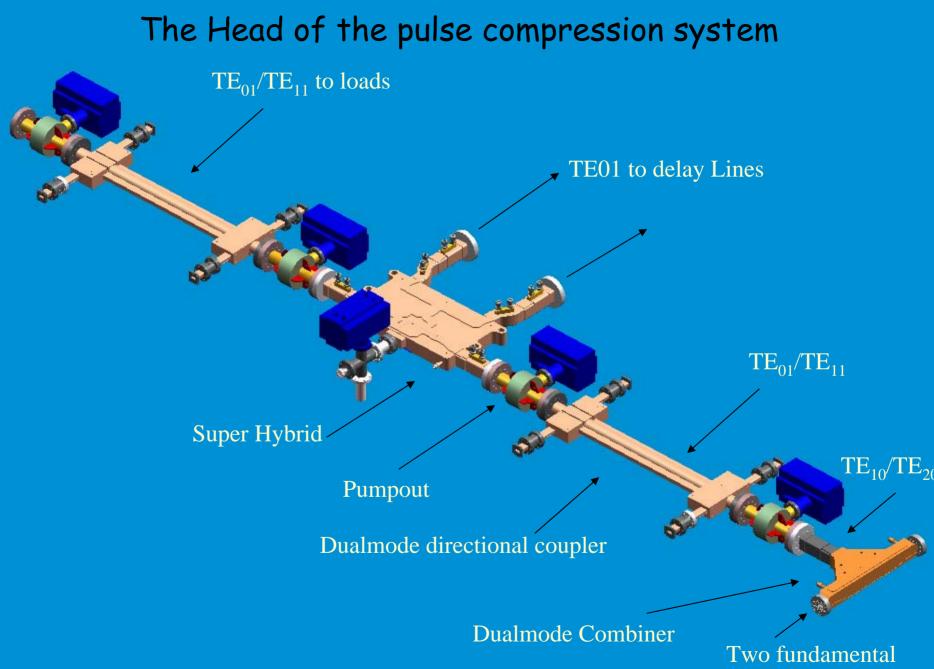
Electric Field breakdown strength in a 16% Group velocity copper waveguide at 11.424 GHz

Pulsed Heating

Pulsed Heating of Copper Surface Vs. Surface H Field for Different Pulse Durations

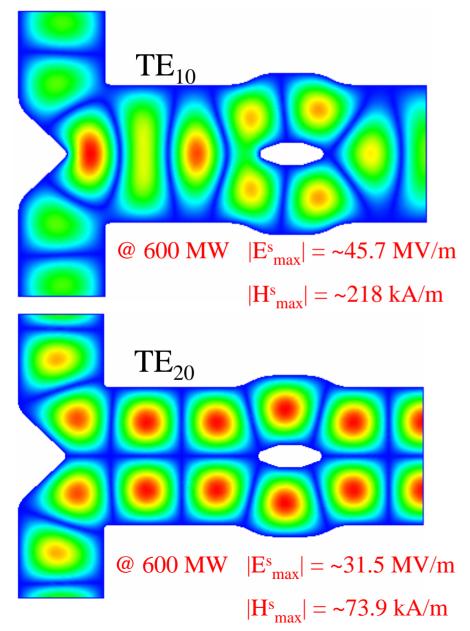


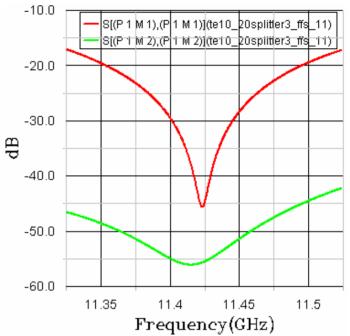


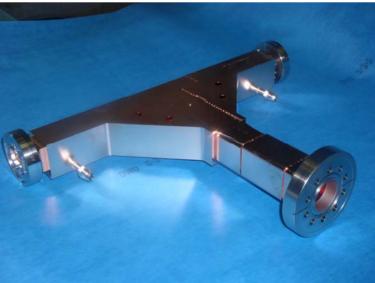


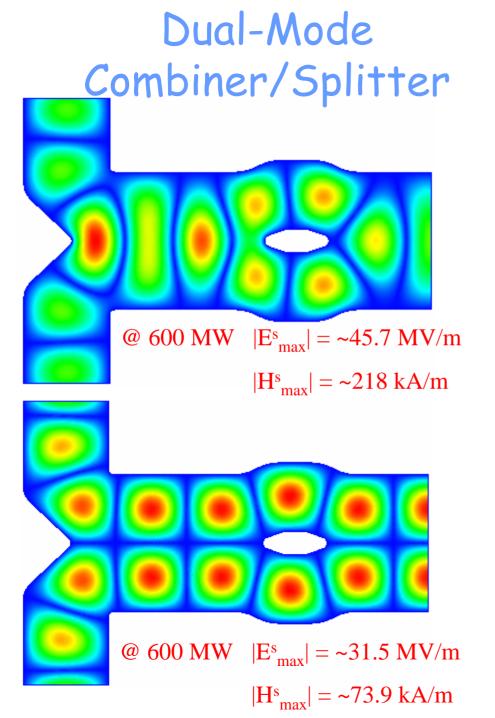
mode inputs

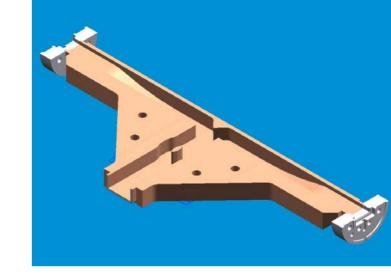
Dual-Mode Combiner

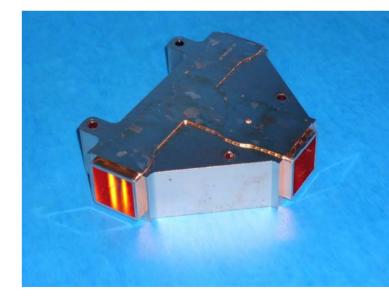


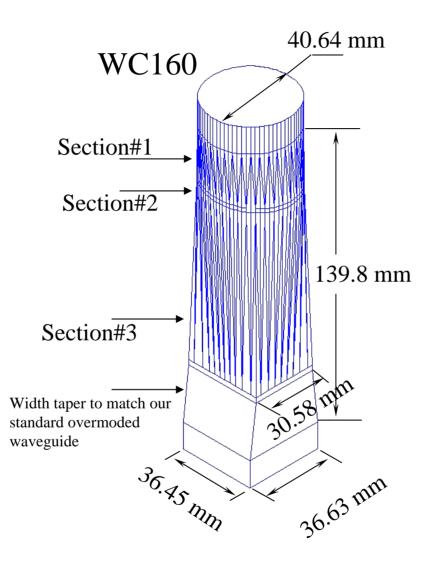


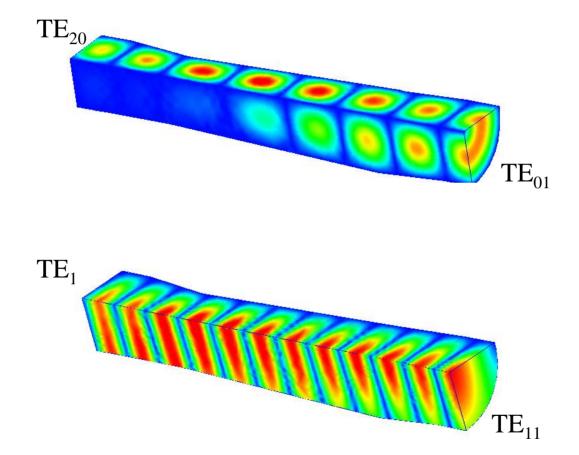


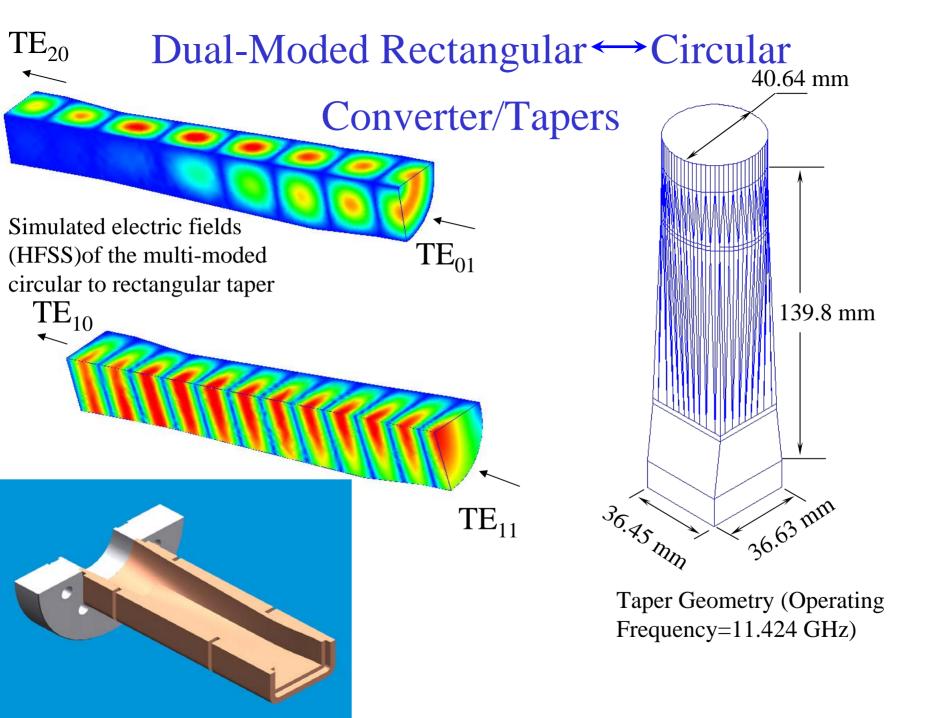




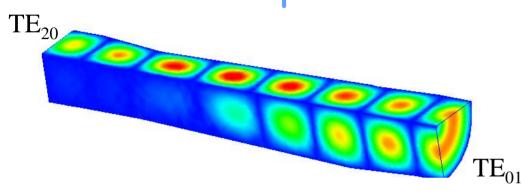


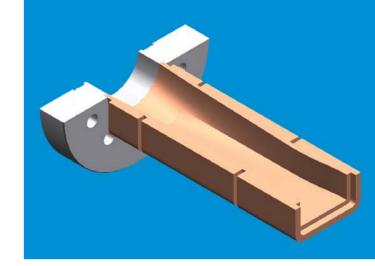




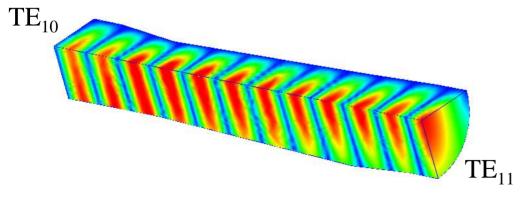


Dualmode Rectangular-to-Circular Taper







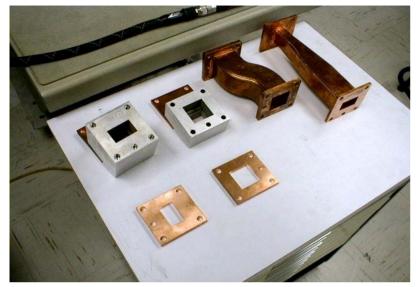


Instrumental components for cold testing of multimode components:

- 1. TE_{11} Mode launcher
- 2. TE_{01} Mode launcher
- 3. Width taper
- 4. Height taper
- 5. Small waveguide sections with different lengths at all waveguide cross sections

We followed a strict methodology of designing these instruments. They had to be simulated with at least three different codes and have a performance that is much better than any component that we have. Of course we can only do that because there is no restrictions on field levels.

Cold Test Components/Calibration Standards



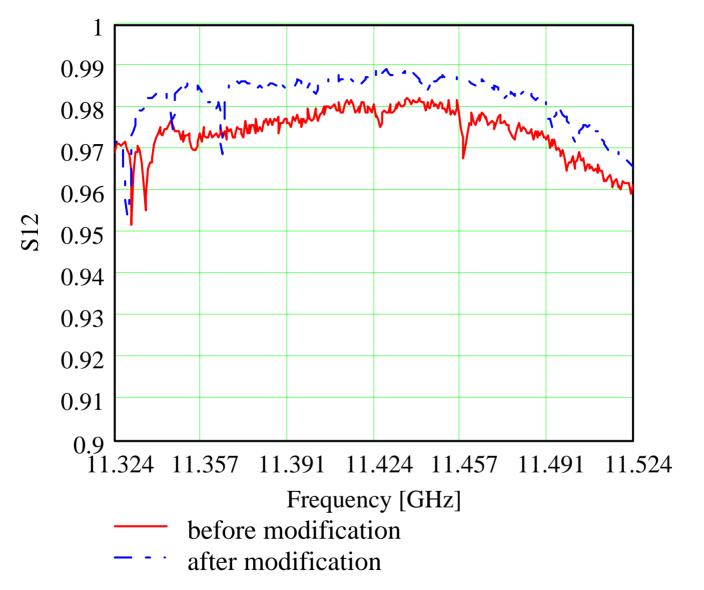
Width Taper, Height Tapers and Jog Mode Converter



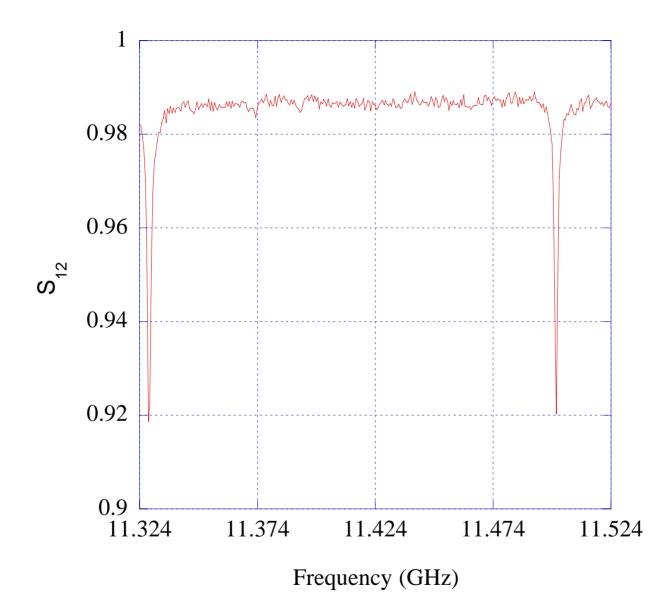
TE₀₁ Mode Converter, TE₁₁ Mode Converter, and Size Taper



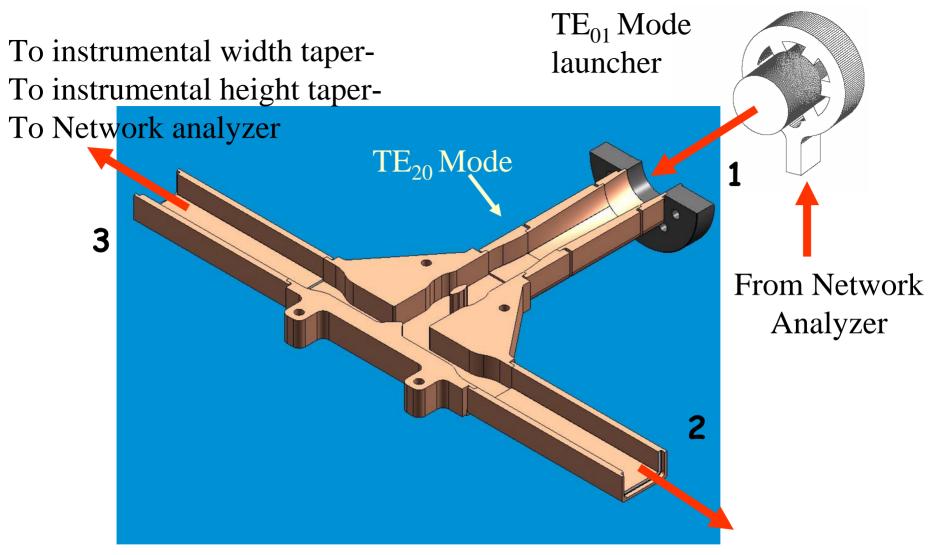
Multimoded Matched Load



Measured S_{12} between the Rectangular TE_{02} mode and the circular TE_{01} mode. These measurements include the response of mode transducers necessary to launch the modes at both ends of the taper.

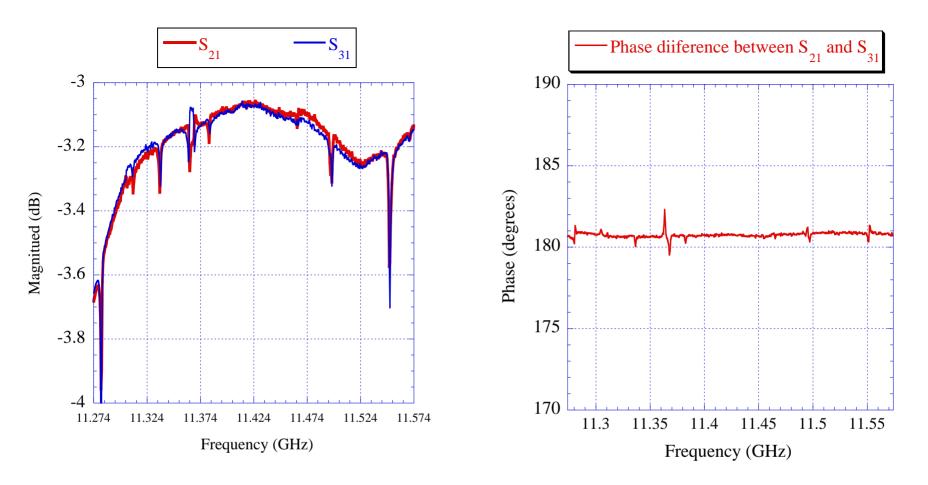


Measured S_{12} between the rectangular TE_{01} mode and the circular TE_{11} mode. These measurements include the response of mode transducers necessary to launch the modes at both ends of the taper.



Cold Test Setup for the Splitter and Circular-to-Rectangular Taper

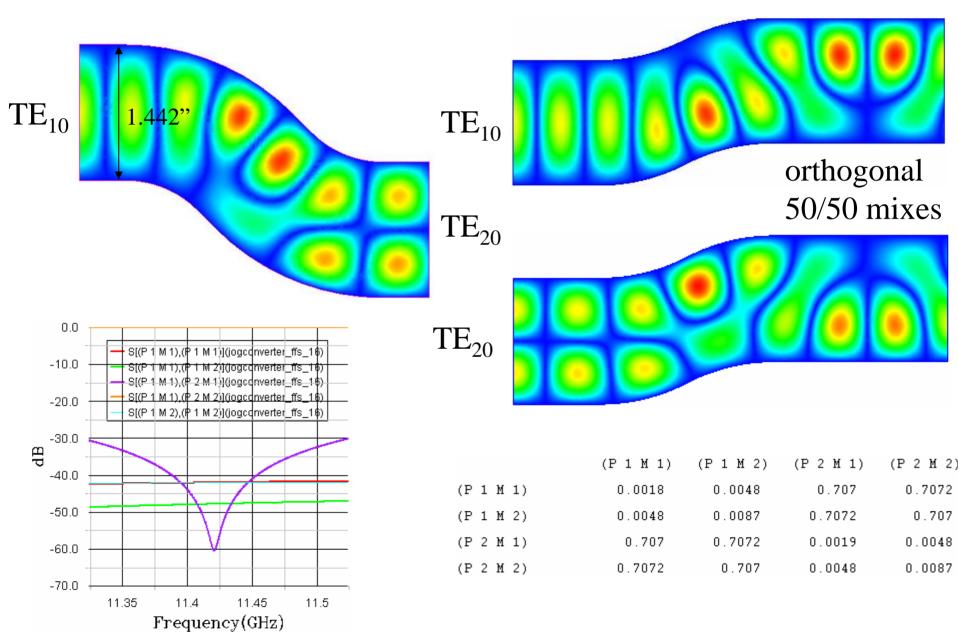
To instrumental width taper-To instrumental height taper-To Network analyzer



Cold Test Results of:

Splitter-Circular to rectangular taper-Wraparound mode converter-instrumental height taper-instrumental width taper. Total losses at 11.424 GHz =1.3%

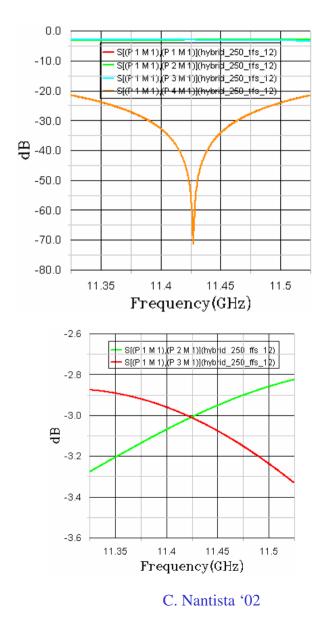
Jog Converter and Mode Mixer

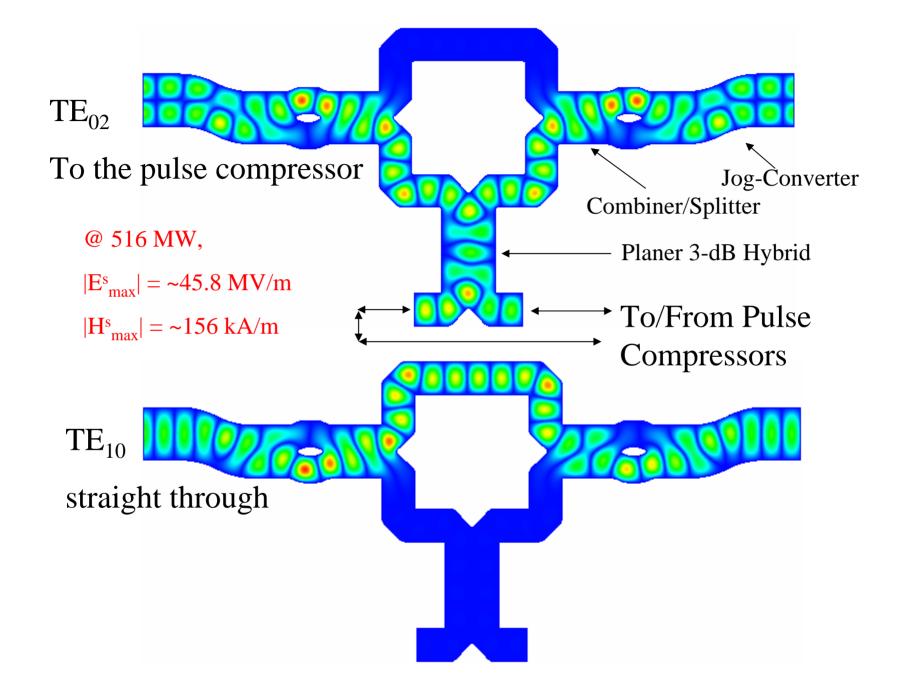


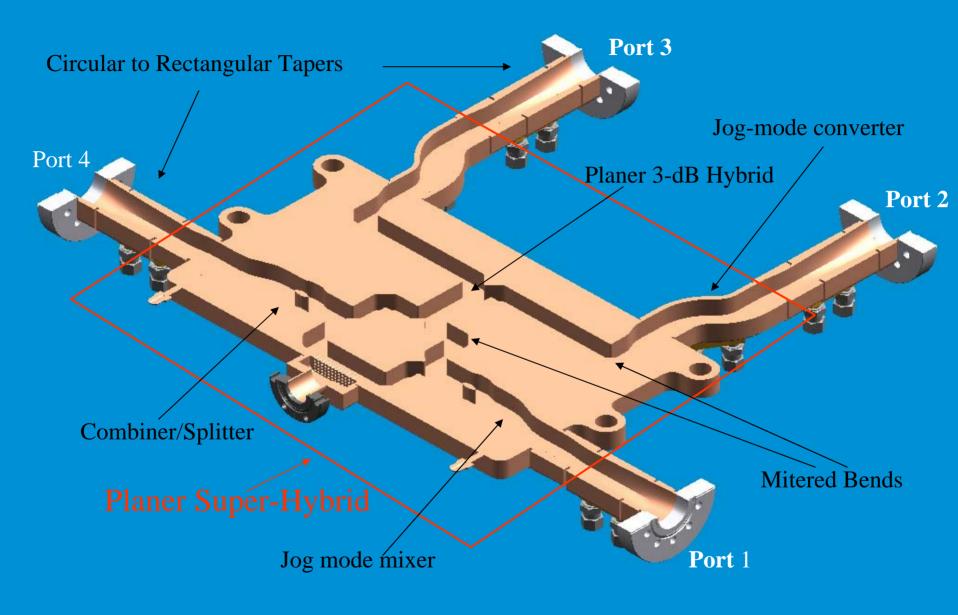
Magic H Hybrid

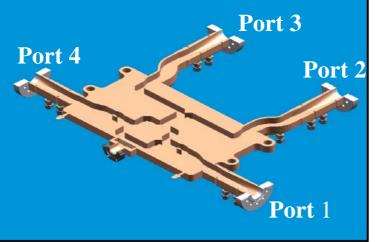
	(P 1 M 1)	(P 2 M 1)	(P 3 M 1)	(P 4 M 1)
(P 1 M 1)	0.0028	0.7071	0.7071	0.0028
(P 2 M 1)	0.7071	0.0028	0.0028	0.7071
(P 3 M 1)	0.7071	0.0028	0.0028	0.7071
(P 4 M 1)	0.0028	0.7071	0.7071	0.0028

@ 600 MW, 1.435" height: $|E_{max}^{s}| = ~45.6 \text{ MV/m}$ 0.900" $|H^{s}_{max}| = ~168 \text{ kA/m}$ 2.3821" 1.4788"

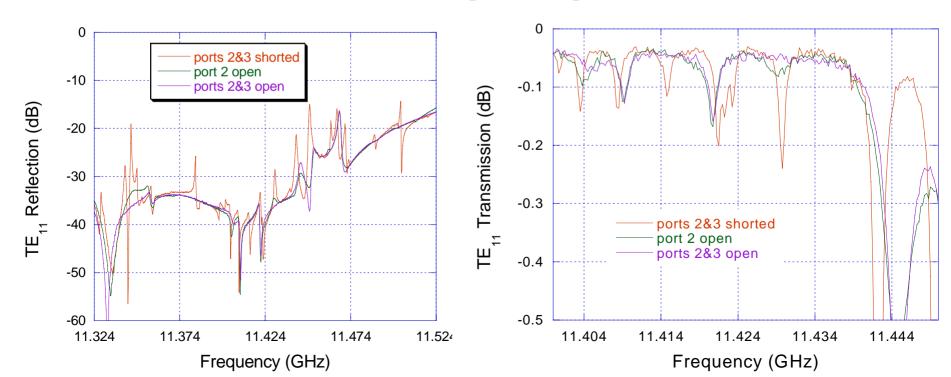


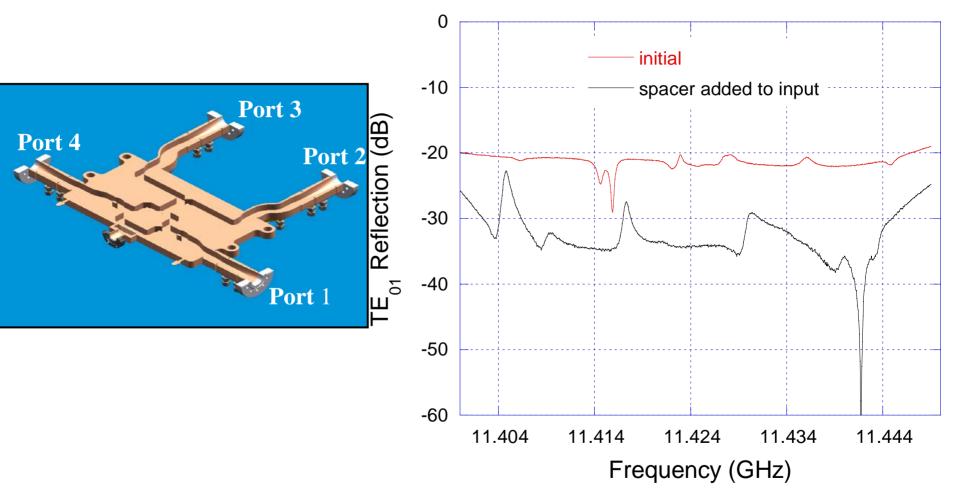




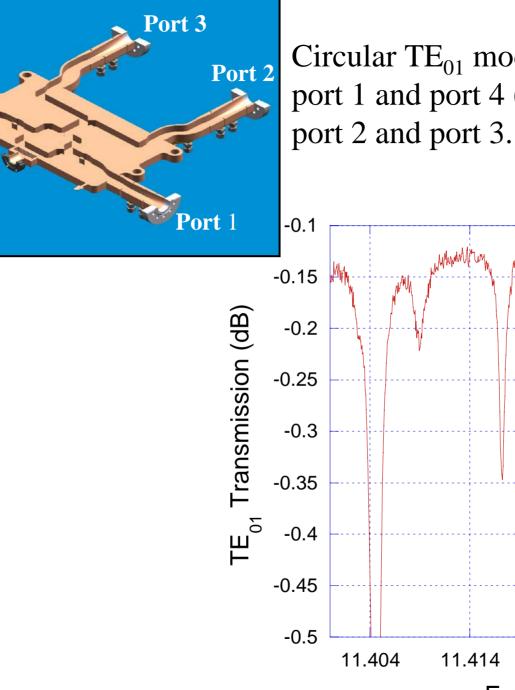


Circular TE_{11} mode transmission and reflection between port 1 and port 4 (S₄₁) for different conditions at port 2 and port 3.



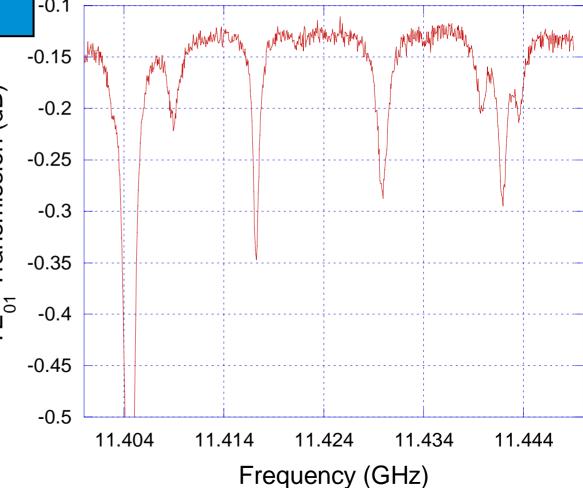


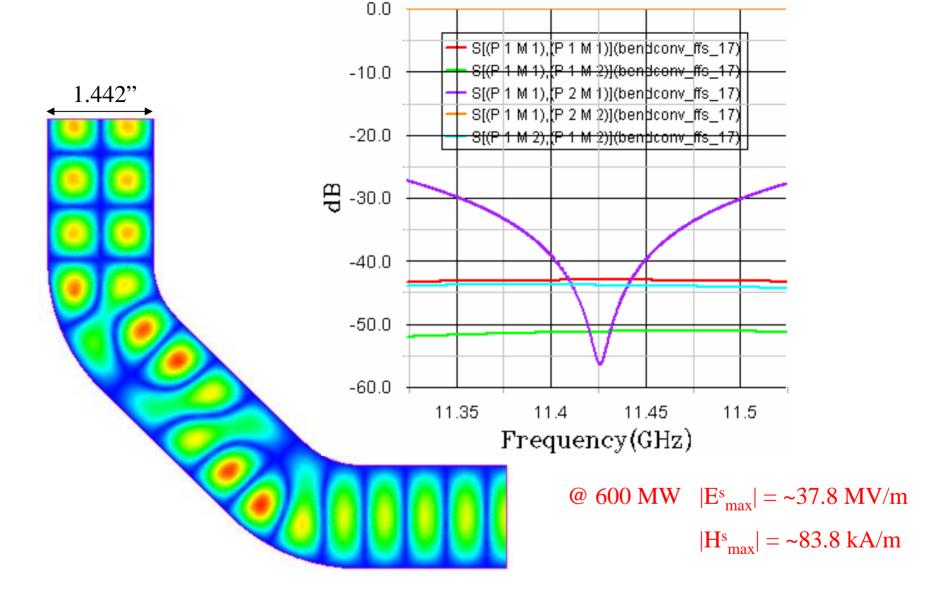
Reflection measurements (S_{11}) for the circular TE₀₁ mode from port 1 while shorting both port 2 and port 3 and matching port 4.



Port 4

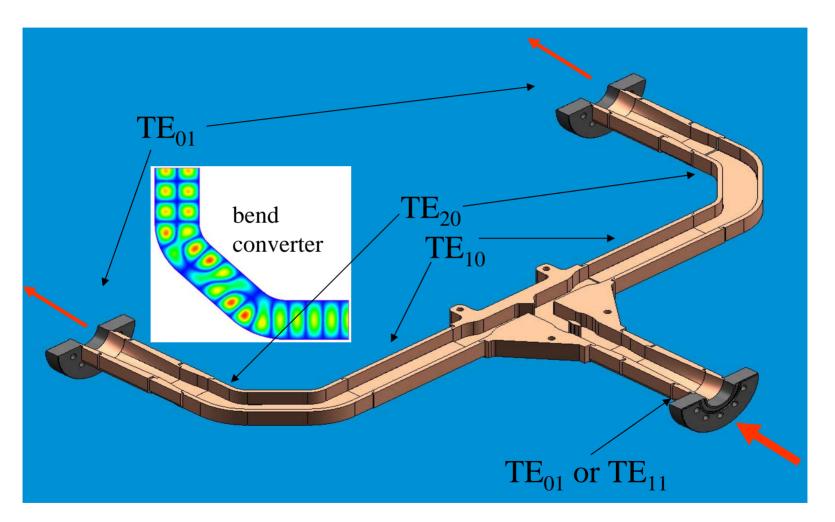
Circular TE_{01} mode transmission between port 1 and port 4 (S₄₁) while shorting both port 2 and port 3.



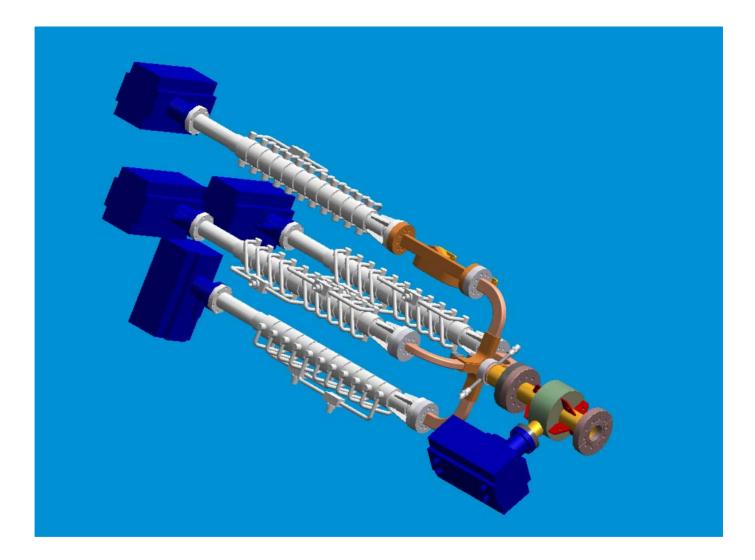


Bend Converter Design

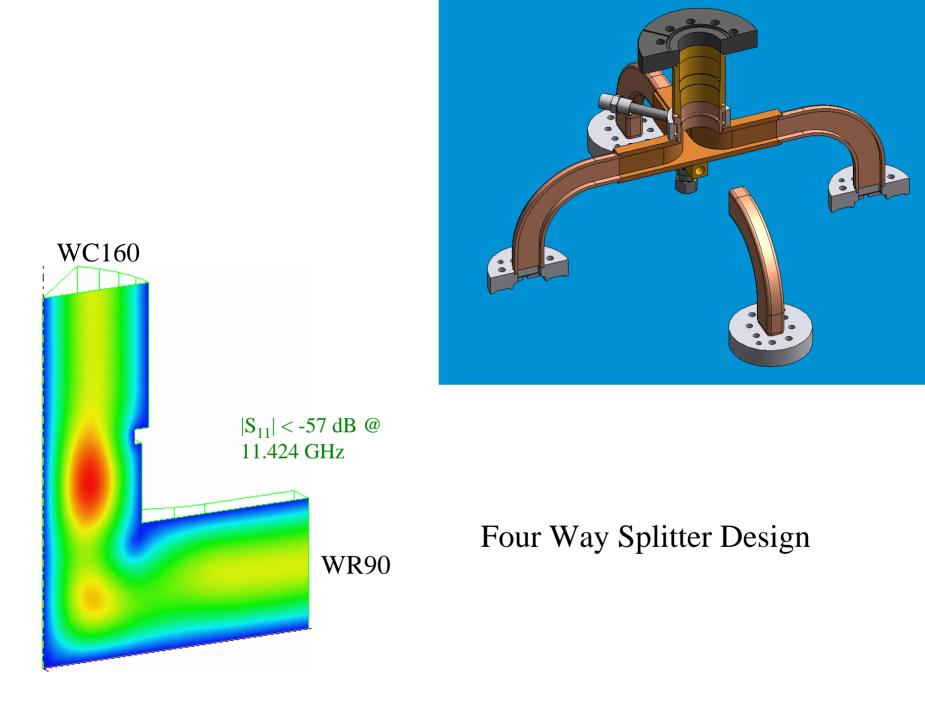
Dual-Mode Splitter



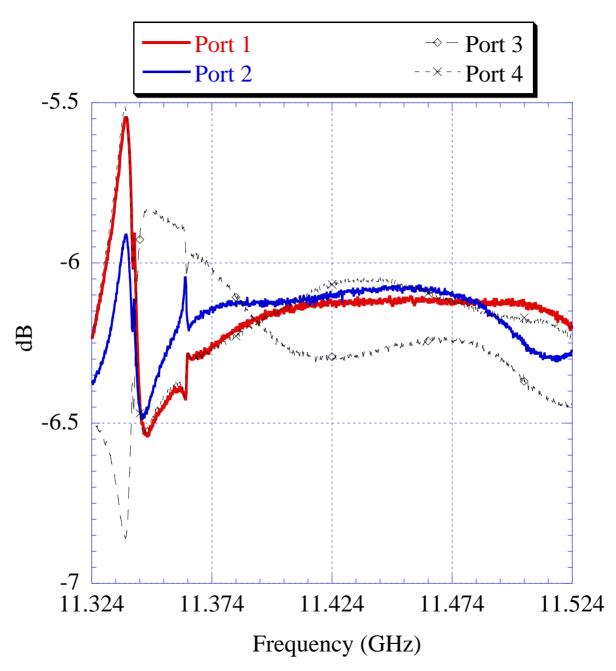
Dualmode Splitter: For either incident mode the power is evenly divided between the two output ports, which launch the TE_{01} .

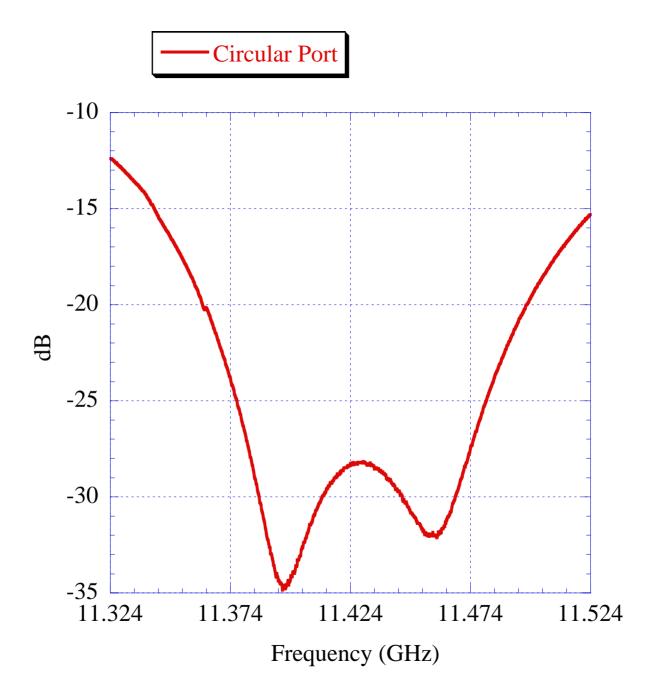


Load Tree: The input power, carried by the TE_{01} mode, is split 4 ways to be absorbed at the loads



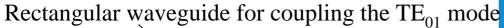
Power Divider

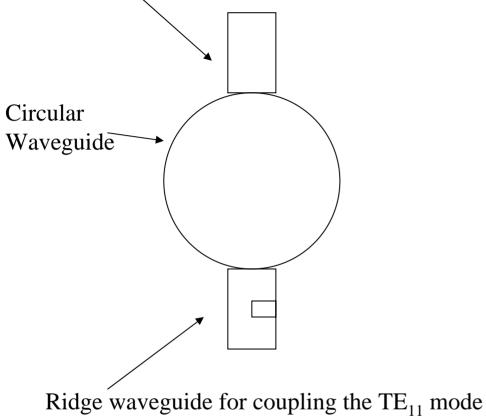


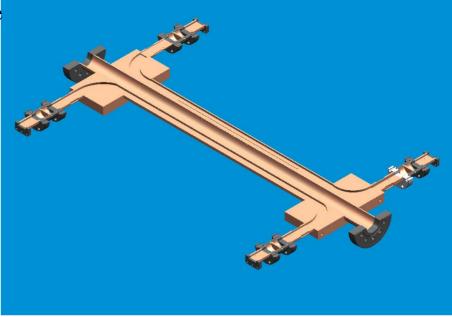




High Power Load Design



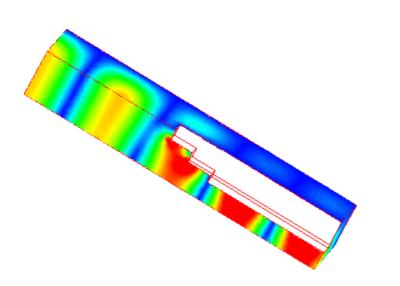


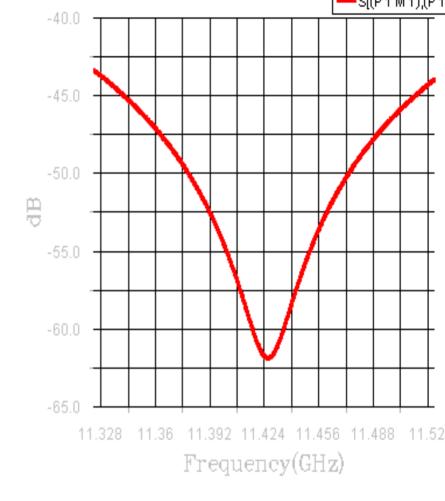


•The waveguide sizes are chosen to match wavelengths between the circular waveguide modes and side waveguide fundamental mode

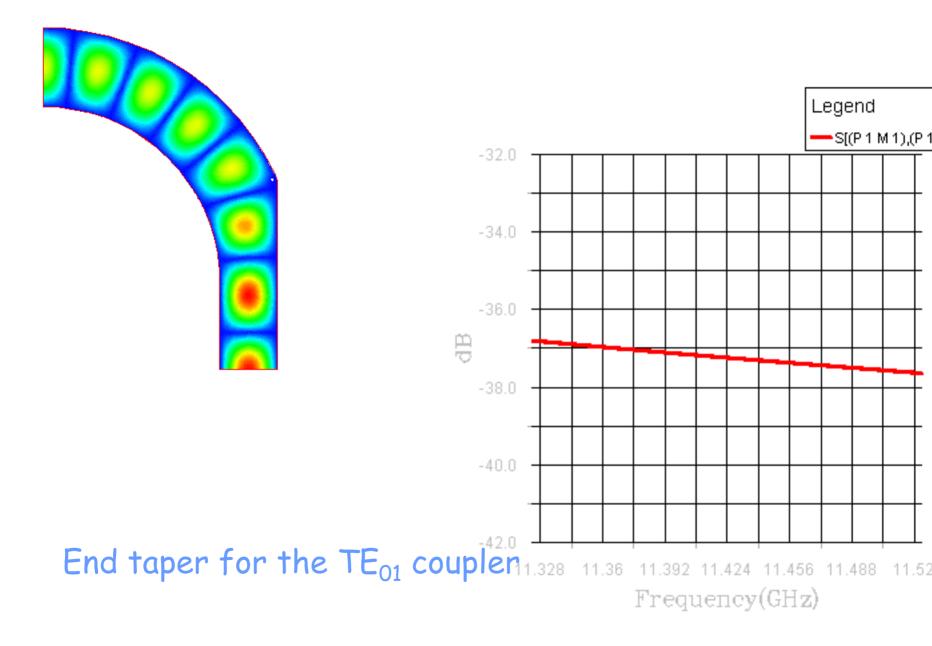
•The coupling hole pattern represents a Hamming window

Dual-moded Directional Coupler





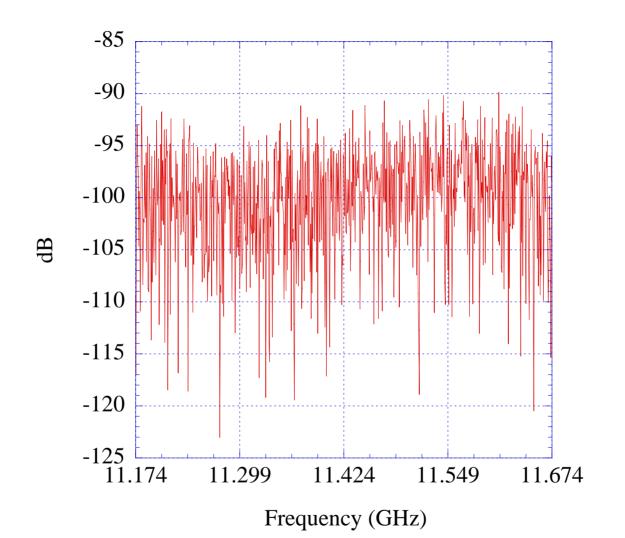
End taper for TE₁₁ coupler



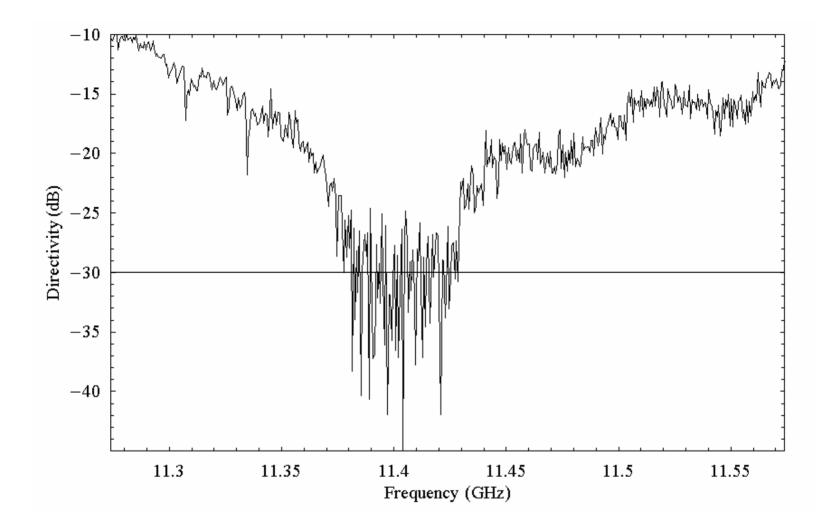


Directional Coupler Cold Test

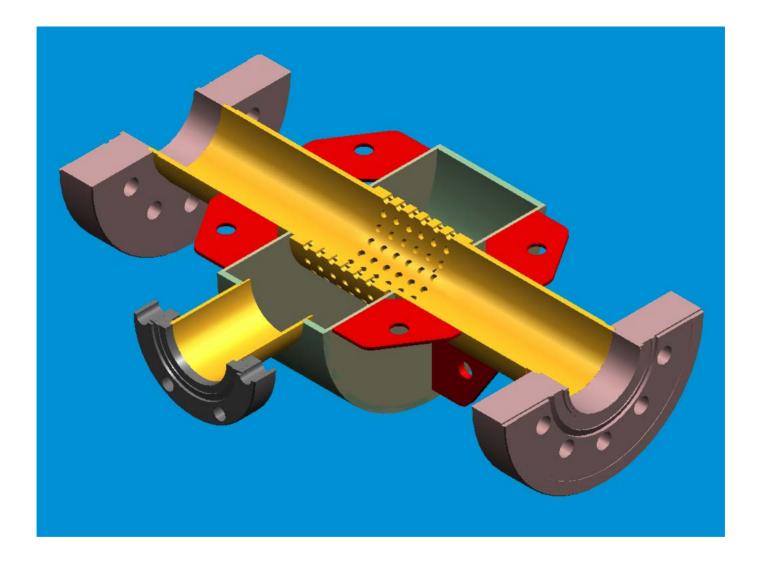




Since the coupling coefficient for the desired mode is -47 dB, Isolation between coupler arm and the unwanted mode is better than -45 dB.

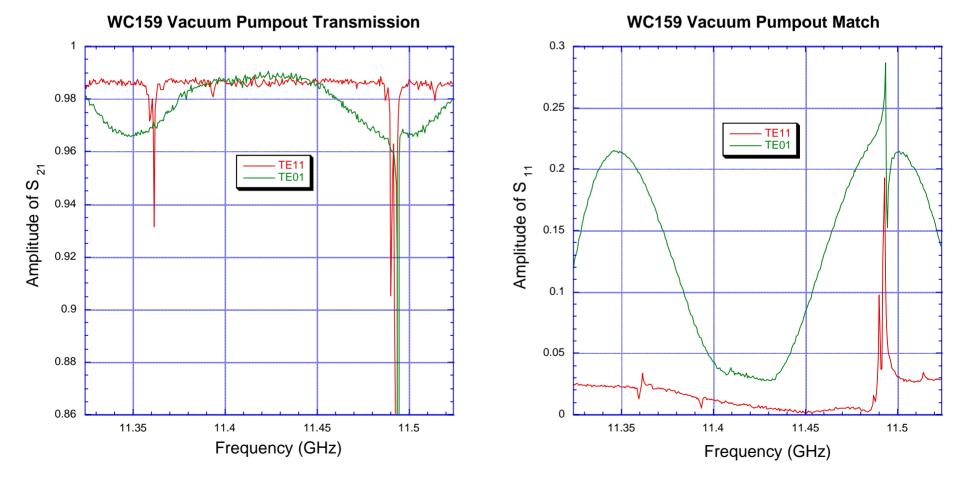


Measured directivity for the TE₀₁ Arm



Pumpout design: the set of holes are designed to cancel any coupling or self-coupling for the TE_{01} and the TE_{11}

Vacuum Pumpout Cold Test



Dual Moded Delay line occupy only half the length of a single moded delay line TE_{02}

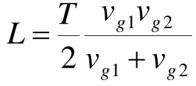
 TE_{01}

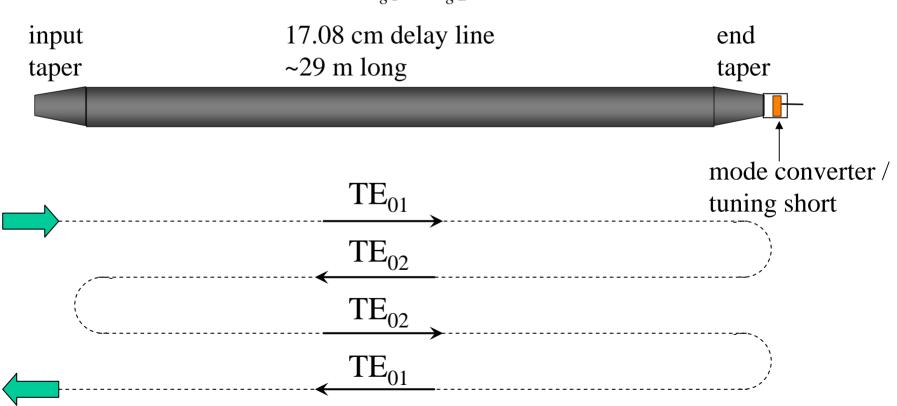
TE₀₁

TE₀₂

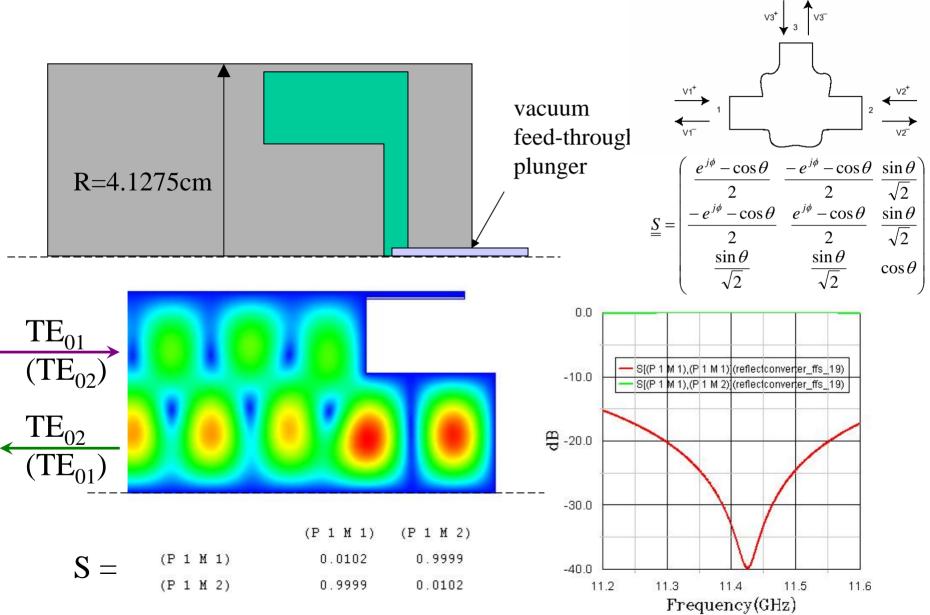
Dual-Moded Delay Line

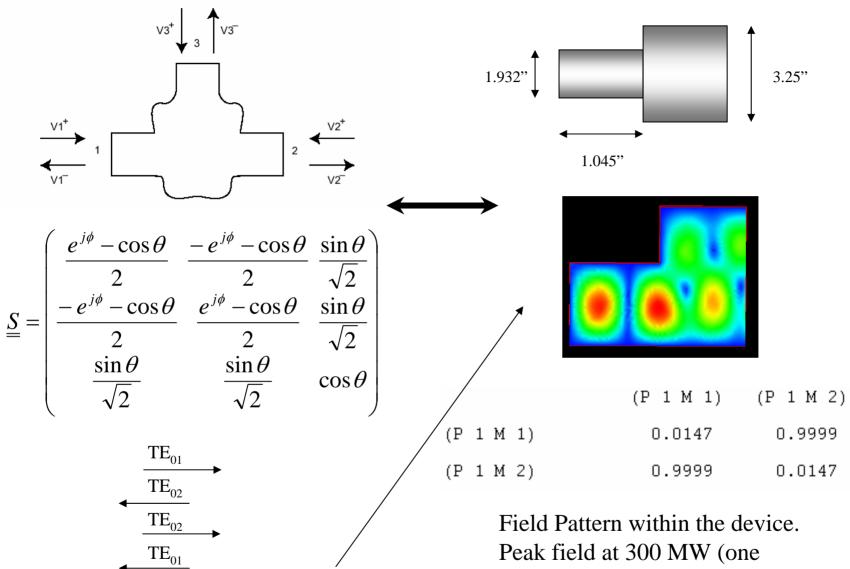
Dual-moding the delay lines cuts their required length approximately in half.





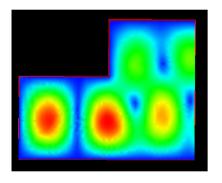
Reflective TE_{01}/TE_{02} Mode Converter

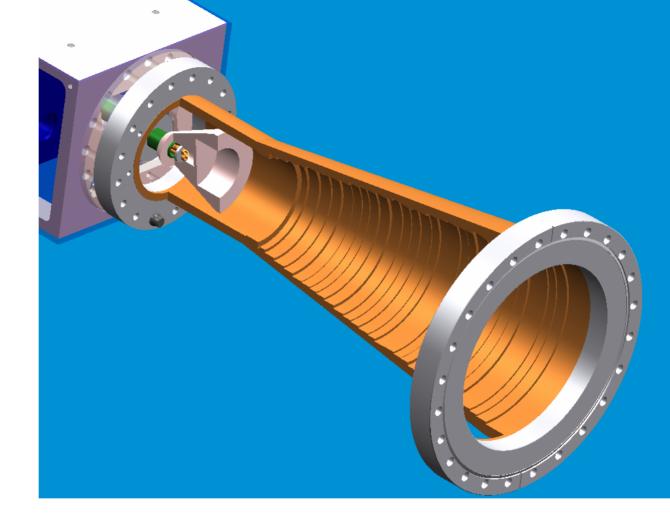




Peak field at 300 MW (one device per transmission line) is 26.6 MV/m. This field is in the middle of the small guide.

End mode converter simulations

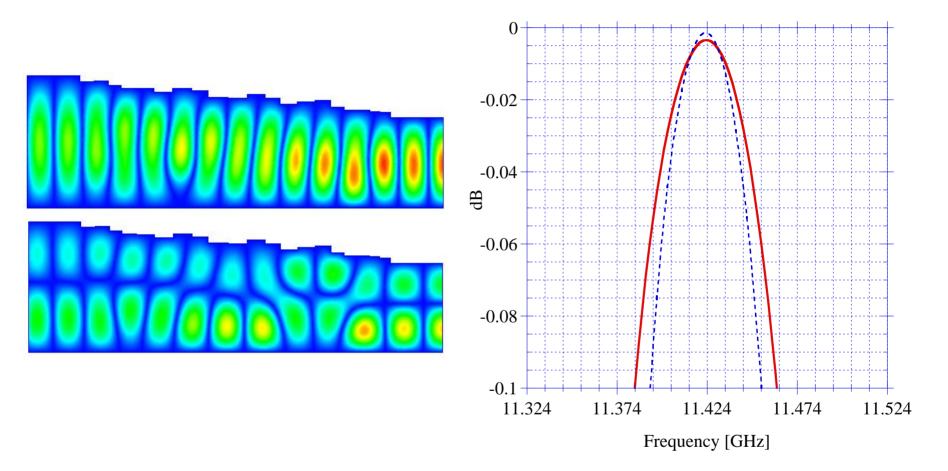




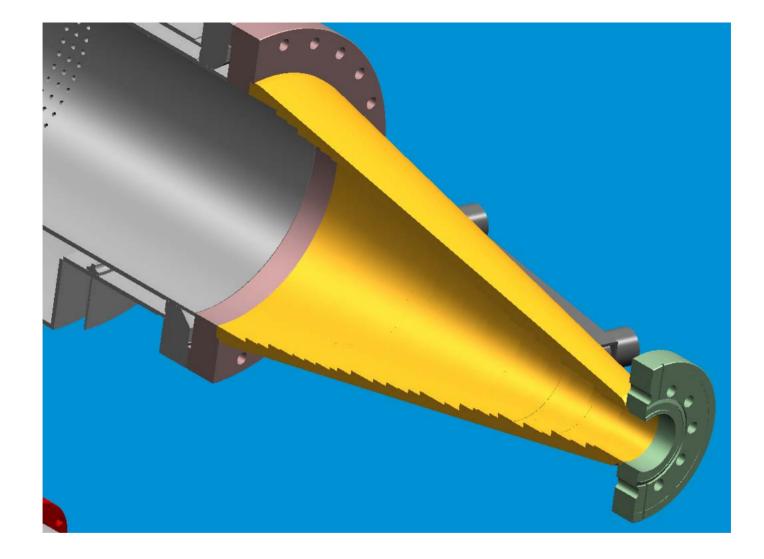
	TE ₀₁ (P 1 M 1)	TE ₀₂ (P 1 M 2)
(P 1 M 1)	0.0147	0.9999
(P 1 M 2)	0.9999	0.0147

End taper and mode converter



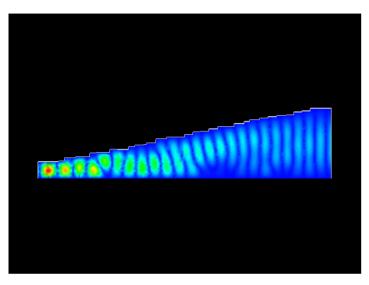


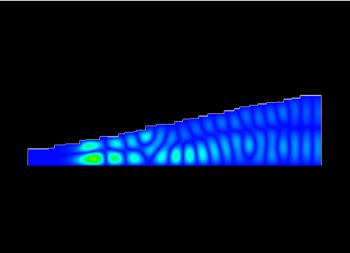
End Taper (before the TE_{01} - TE_{02} Mode converter)

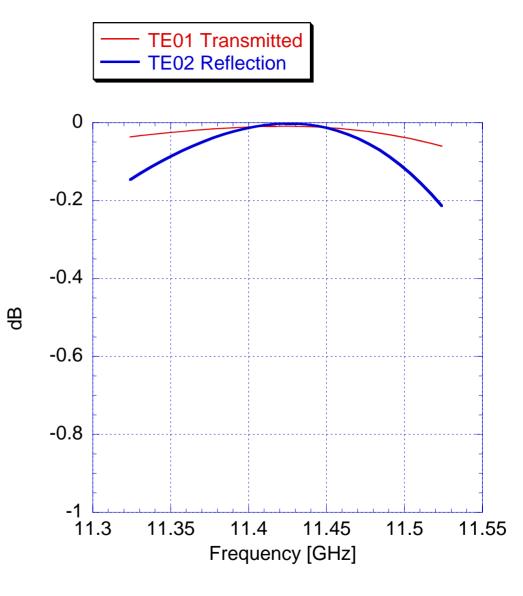


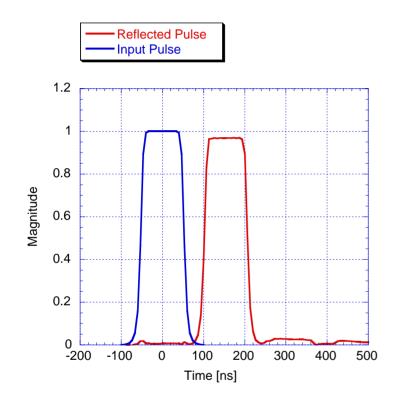
Input taper design

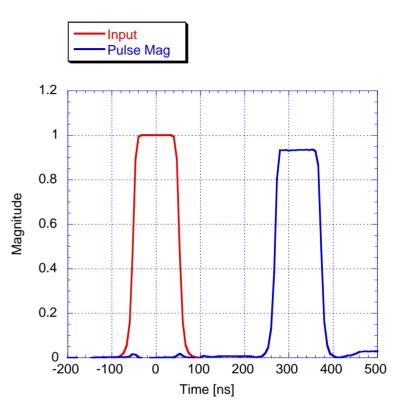
Input Taper for a Dual-Moded System











Measured Delay through 75 feet of WC475 waveguide terminated with a flat plate. The round trip delay time is 154 ns Measured delay through 75 feet of WC475 waveguide terminated with a TE01-TE02 Mode converter. The round trip delay time is 320 ns

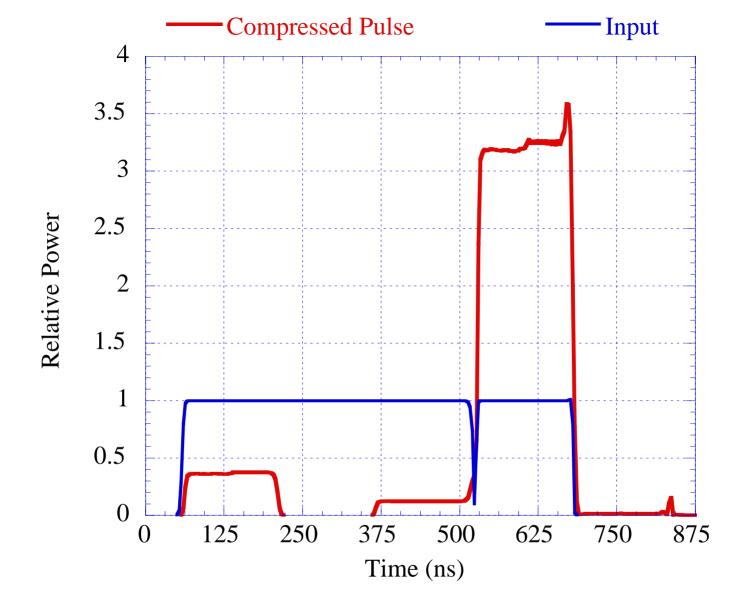




Multimoded Taper Cold Test

Output pulse (delayed by 15 ns 4 round trips) ---- Input pulse 1.2 0.8 S₁₁ Magnitude 0.8 Pulse Amplitude 0.6 Data Mag 0.6 0.4 0.4 0.2 0.2 0 0 -0.2 11.3 11.35 11.4 11.45 11.5 11.55 -300 -200 -100 100 200 0 300 Freq GHz Time(ns)

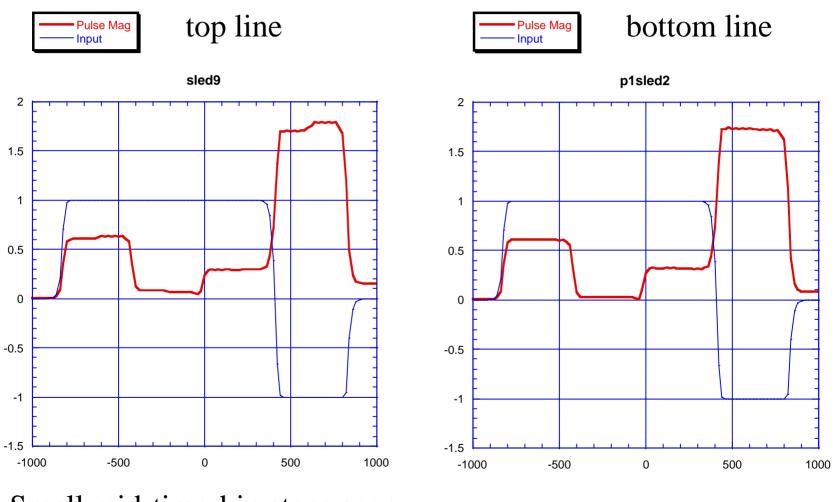
Measured frequency response and constructed time response of the dual-moded taper assembly.



Measured Response of the dual-mode SLED-II Pulse compression system at a compression ratio of 4. Delay line length is ~35 feet. Output pulse width is 150 ns.

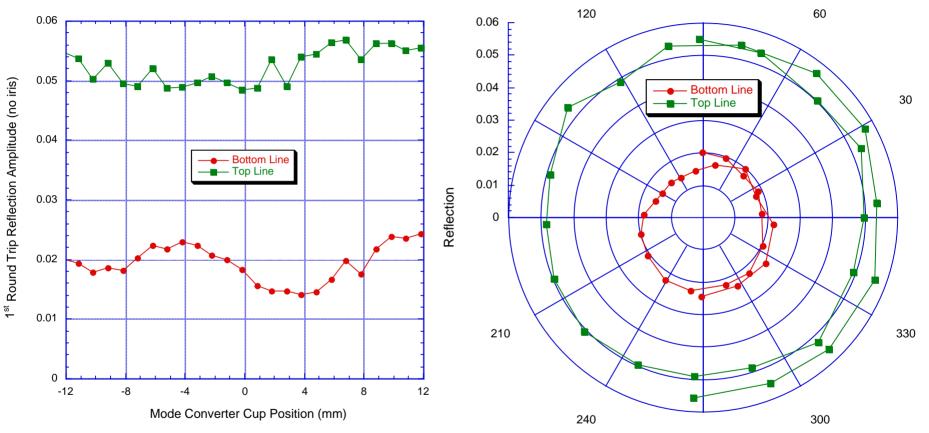
Delay Line Cold Tests

Delay Line Cold Tests



Small mid-time-bin steps seen (mode impurity).

Spurious reflection after first round trip (irises removed)

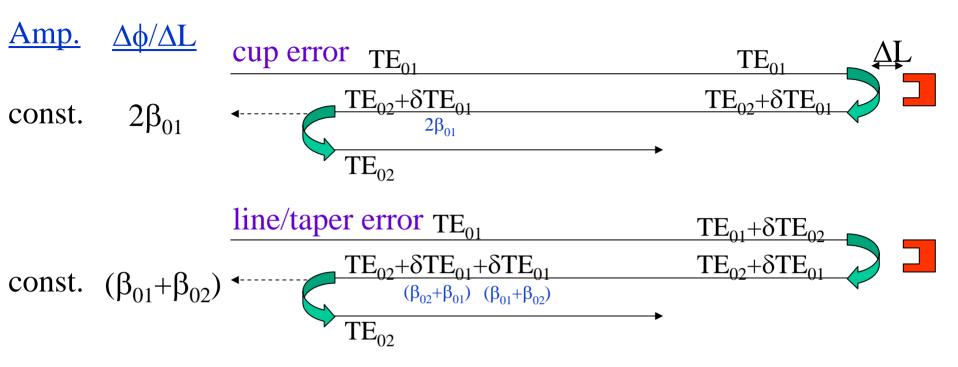


Amplitude oscillation due to interference with slight wrap-around mode launcher mismatch (~-48 dB).

²⁷⁰ TE01 mode contamination bottom line: ~-34 dB top line: ~-26 dB

90

0



The wavelengths for these phase oscillations are 14.2 mm and 16.1 mm, respectively.

Measurements show a phase cycle over ~ 16 mm, indicating mode contamination predominantly from the delay lines and tapers.

Slight amplitude oscillations reveal a very small fixed mismatch in wrap-around mode launcher.

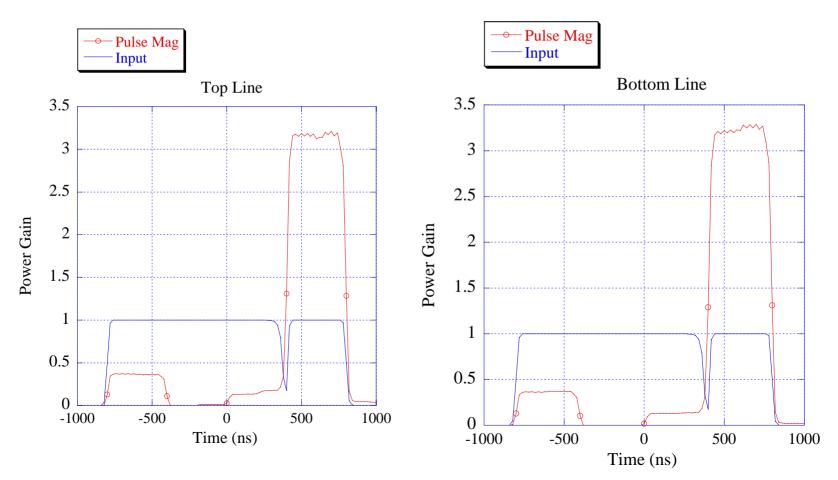
Non-periodic amplitude change can be attributed to longer range beating between a small cup error contamination and that from the lines/tapers (60.3 mm).

Problem Fixed by:

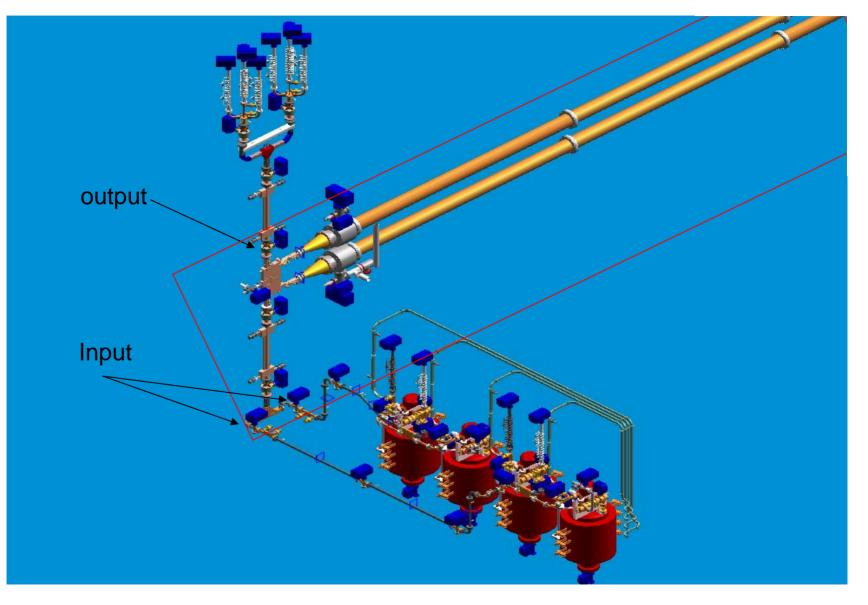
•Permutations of tapers

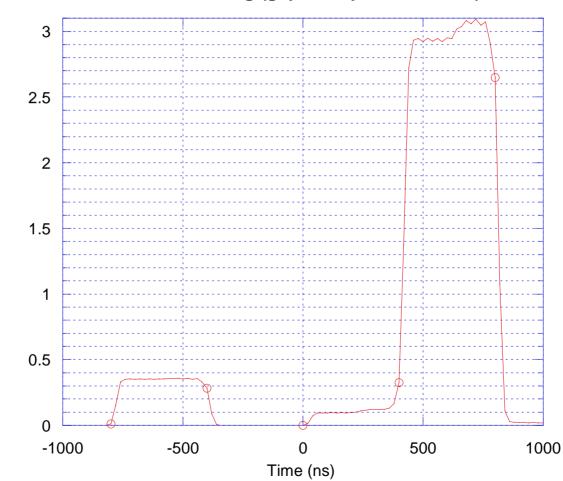
•Adjusting iris distance

•Choosing good resonant position for tuning plunger (3 within range of motion).



System test (1)



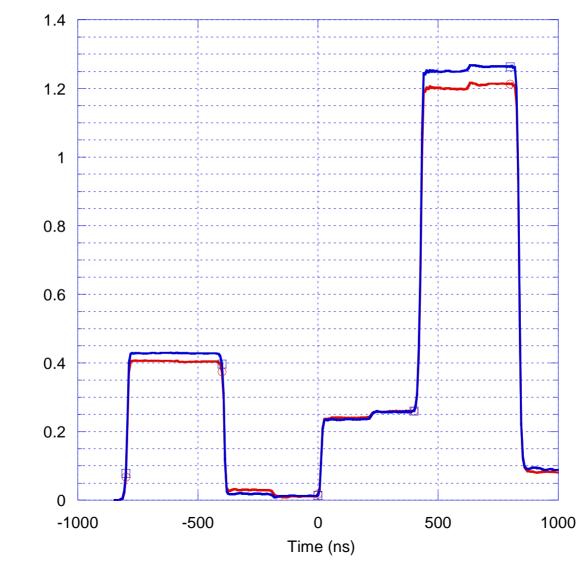


Before Closing (gap on top line~1.5 mm)

Power Gain

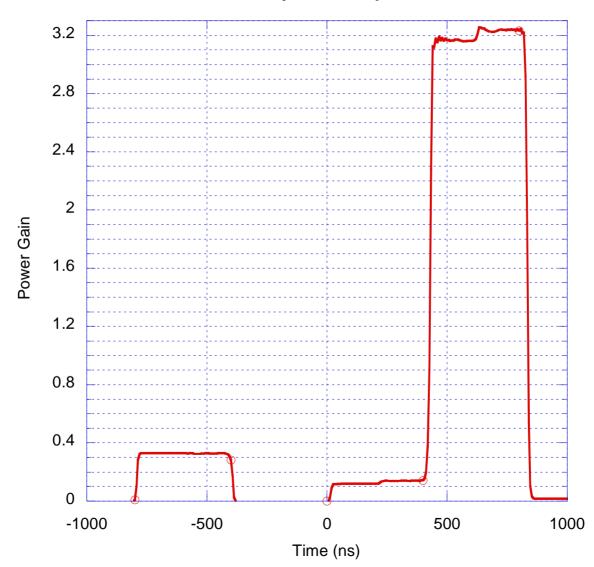


Signal Amplitudes at the Launcher

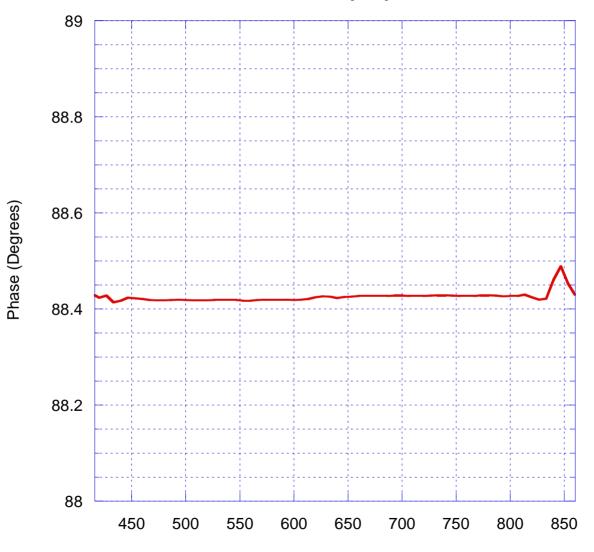


Pulse Mag

Total System Response

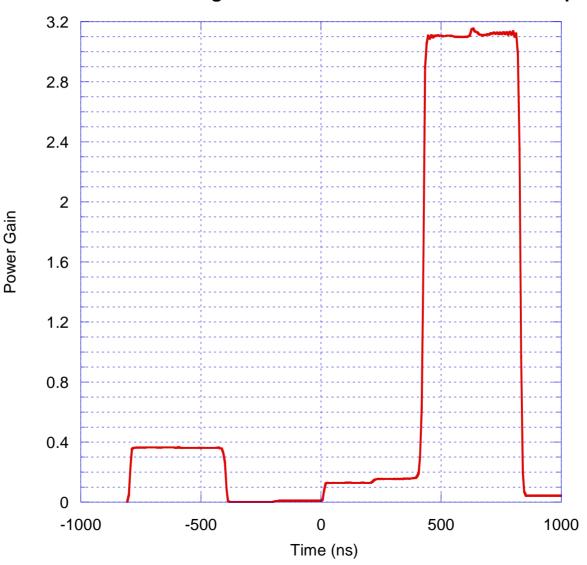


Phase of output pulse

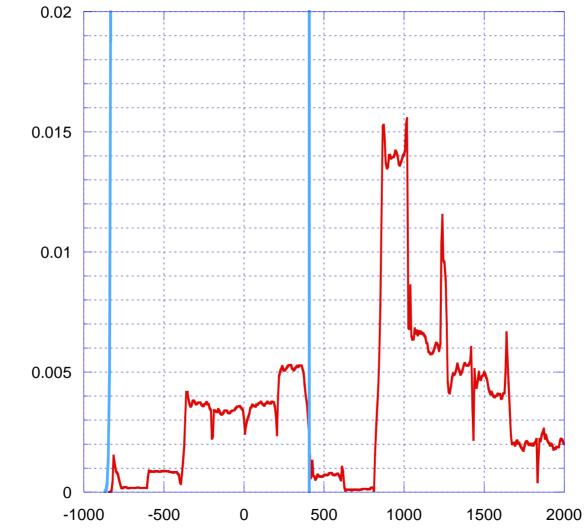


Time (ns)

Measurments Throgh The TE01 Arm of the Directional Coupler

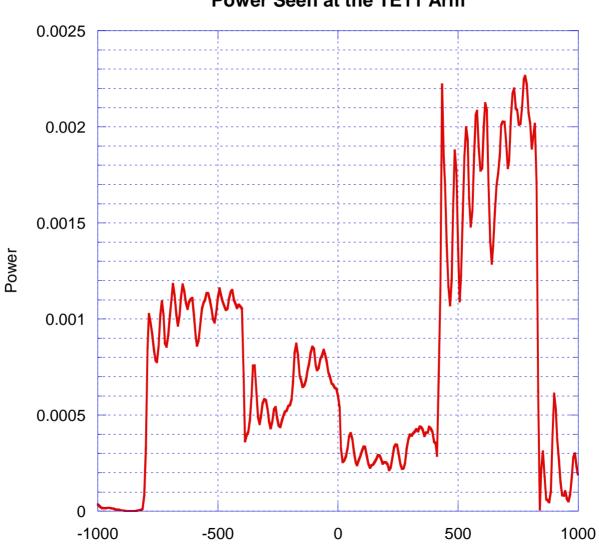






Reflected Power

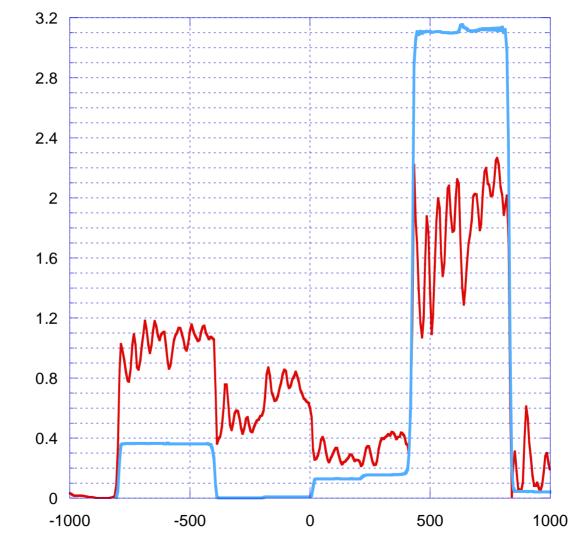
Time (ns)



Power Seen at the TE11 Arm

Time (ns)

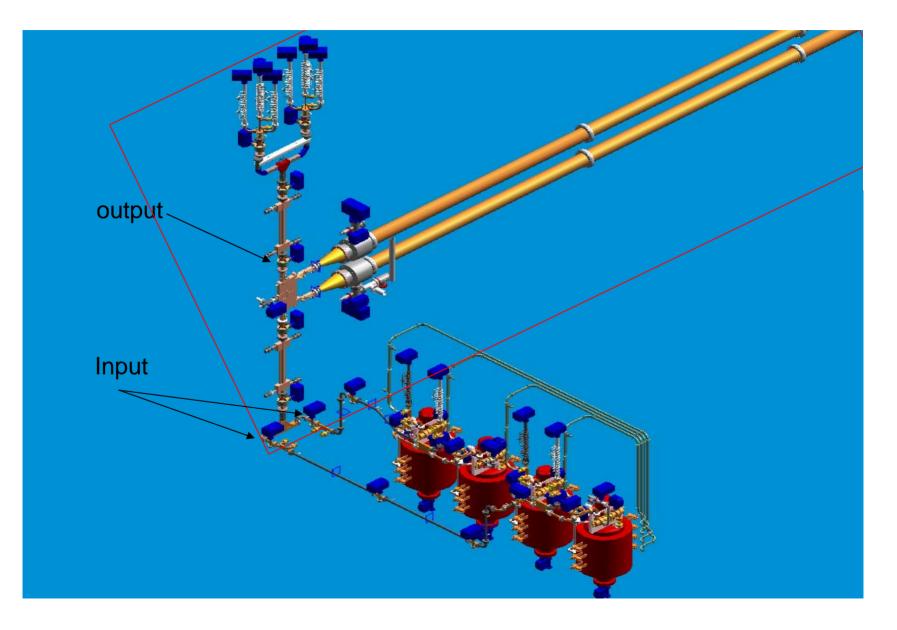
Comparison between the signal seen at the TE11 Arm with the signal at the TE01 Arm



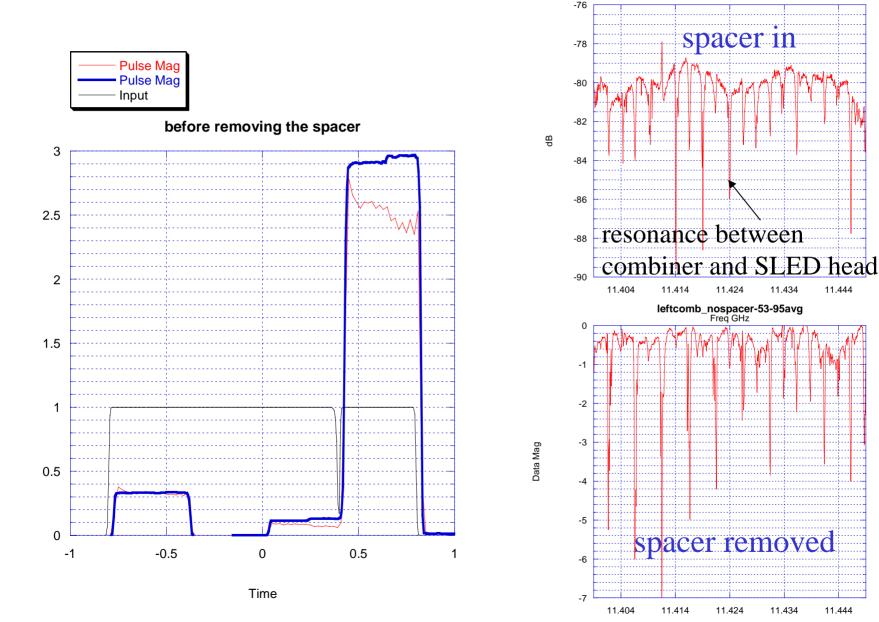
Power

Time (ns)

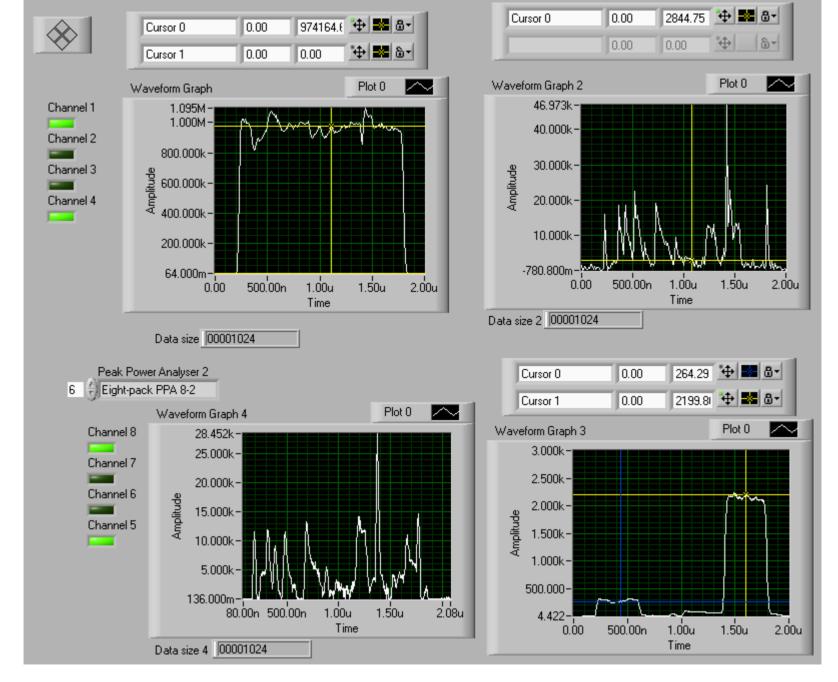




System Cold Tests



Frea GHz



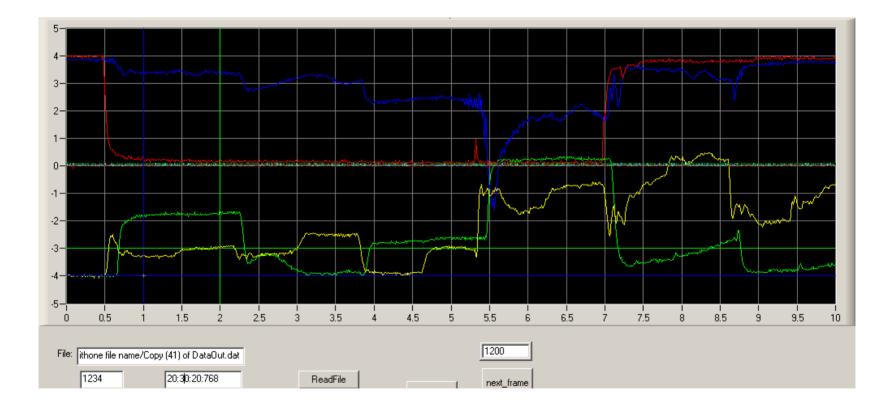
16 steps 2 klystrons

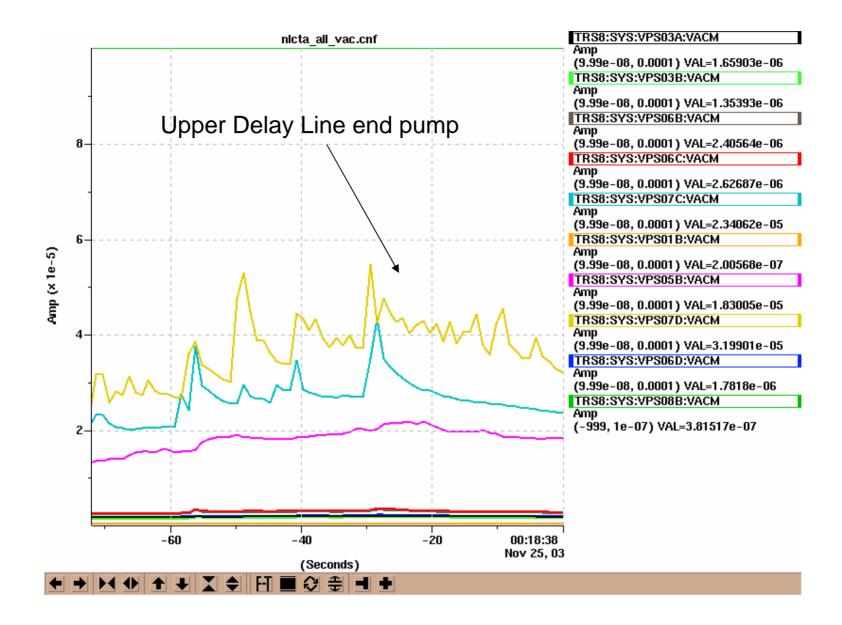
High Power Experiments

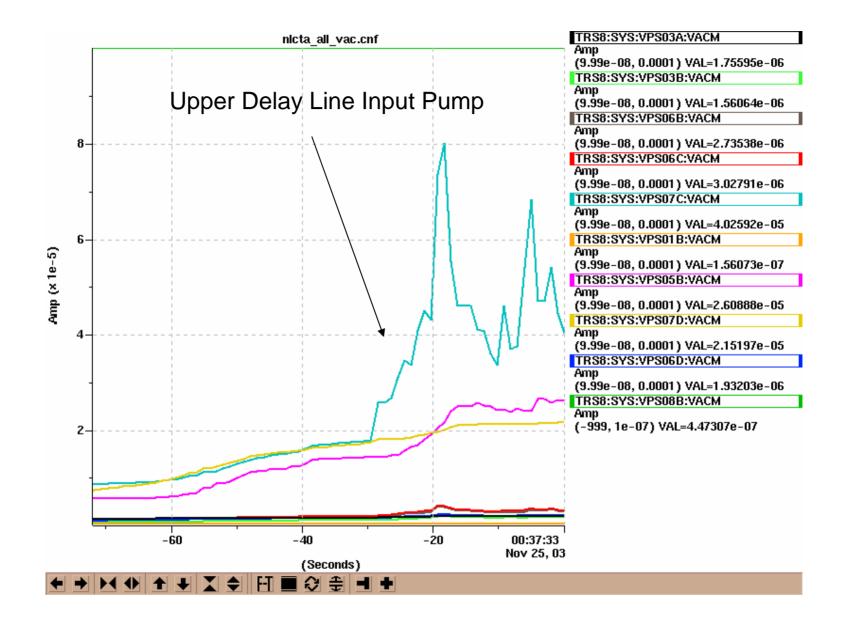
8-Pack Phase 1

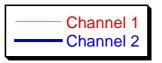


After pulse breakdown (Loads)

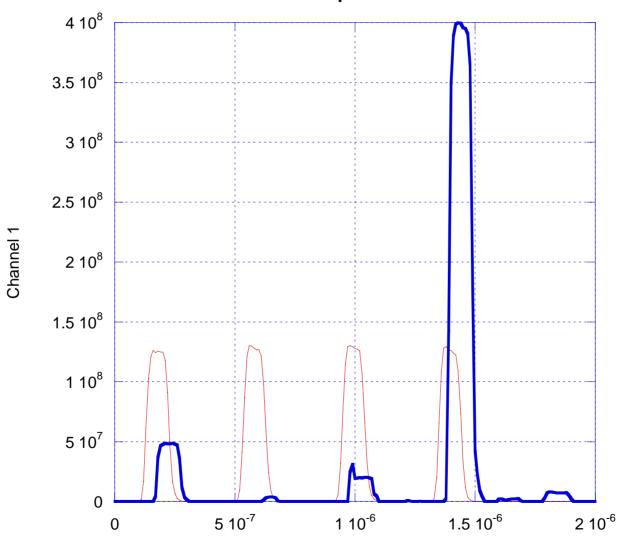


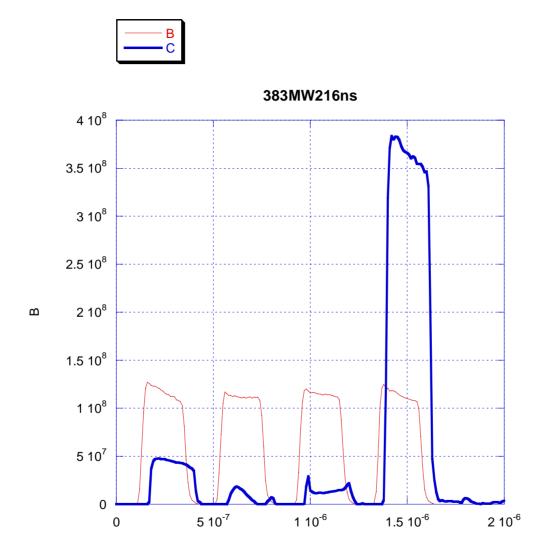




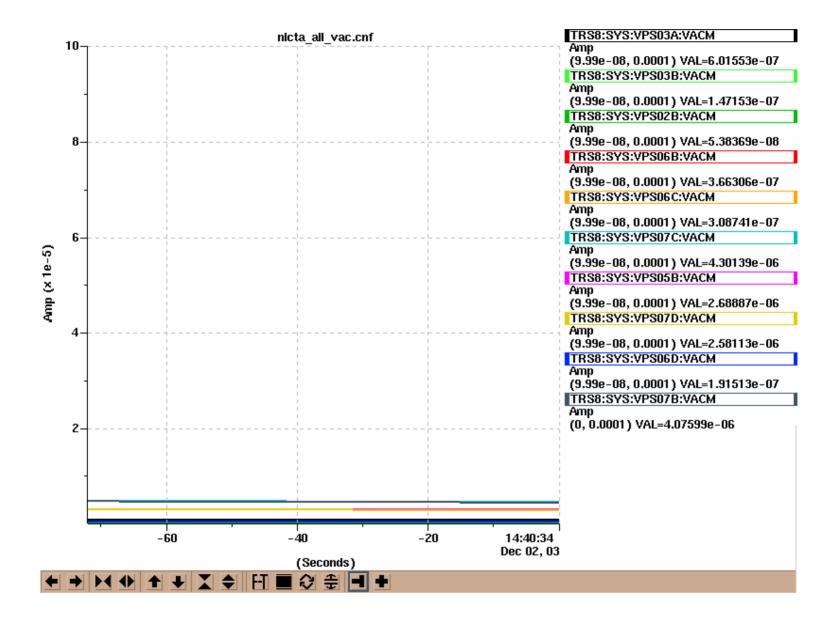


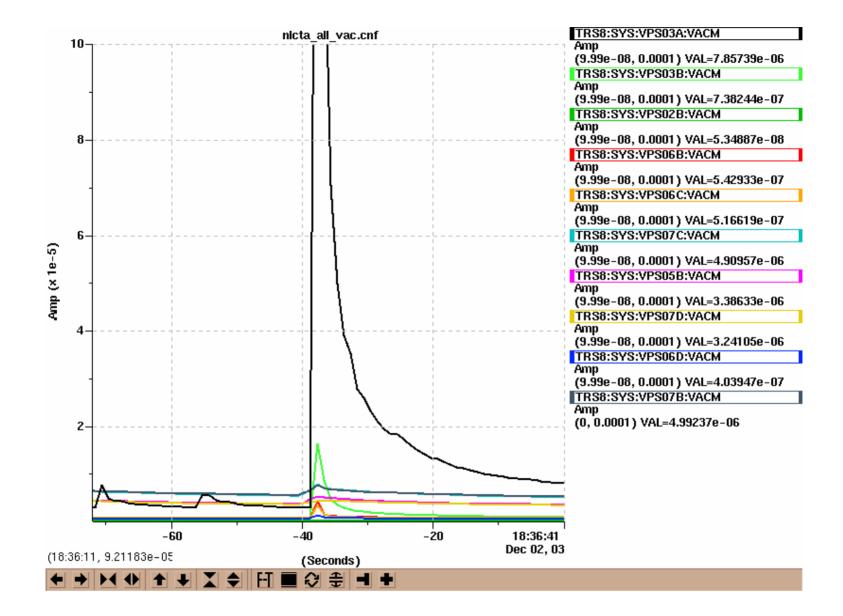
400MWpoint100ns



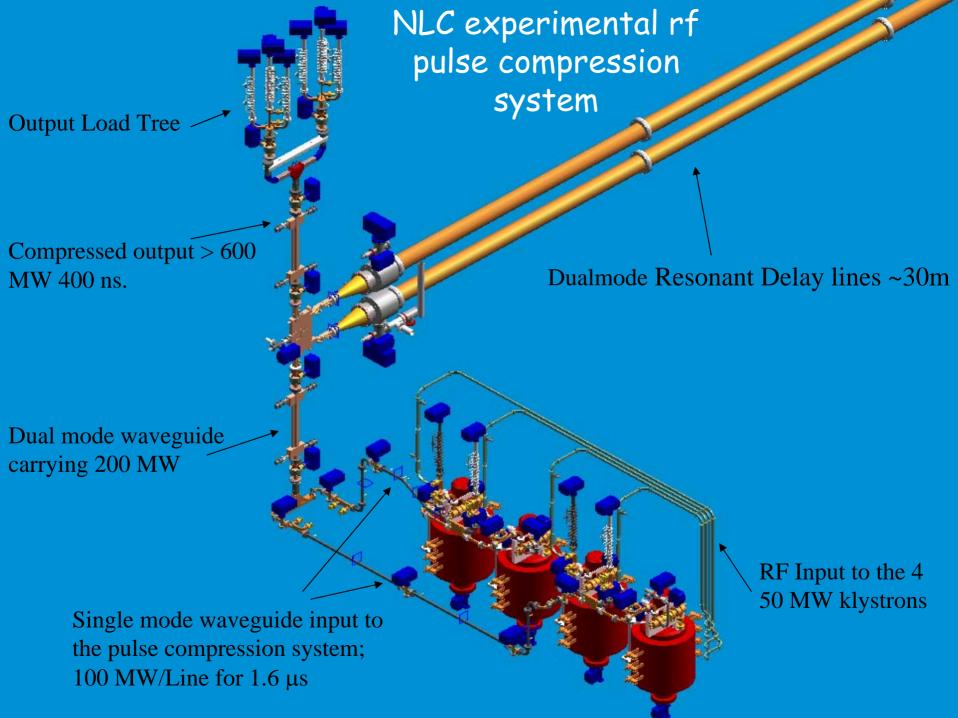


А



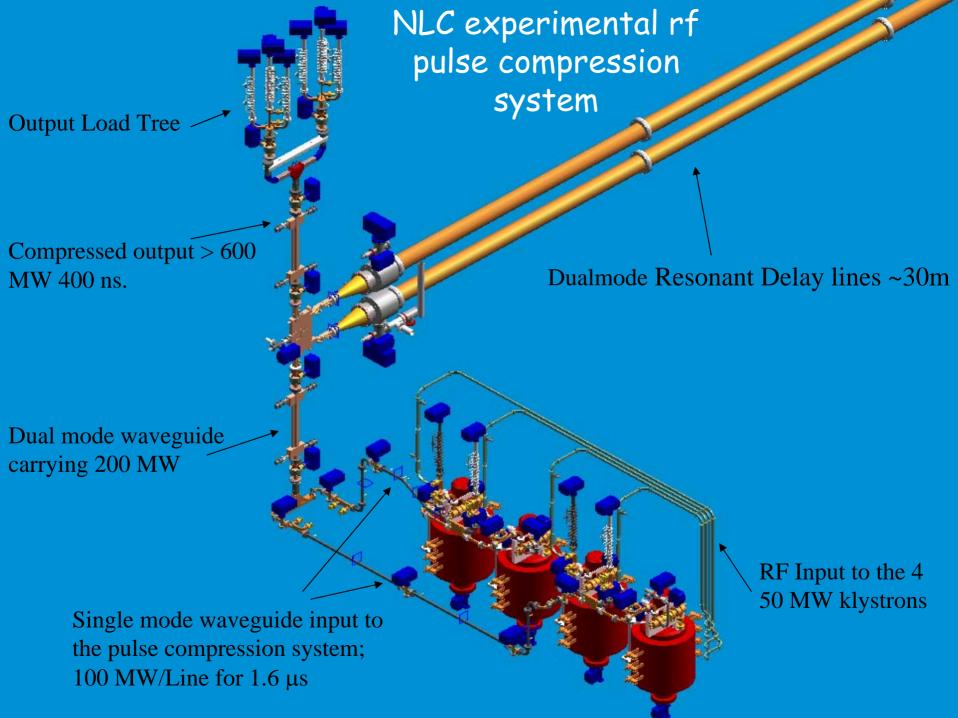


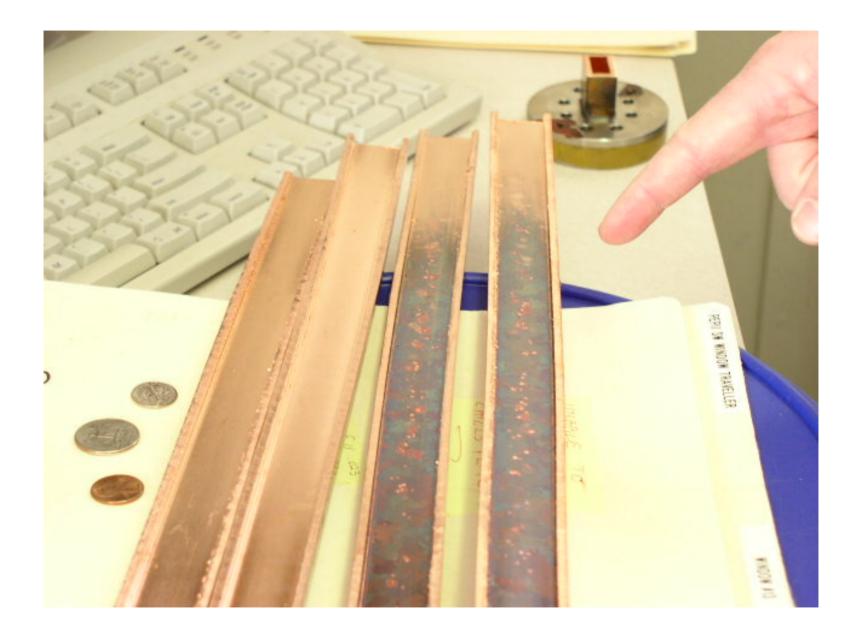
421 MW 360 ns

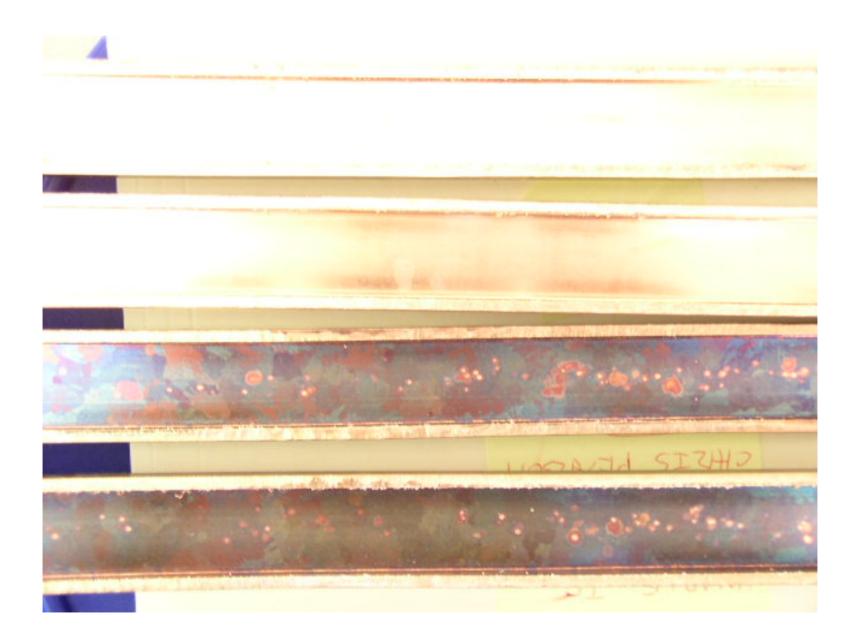


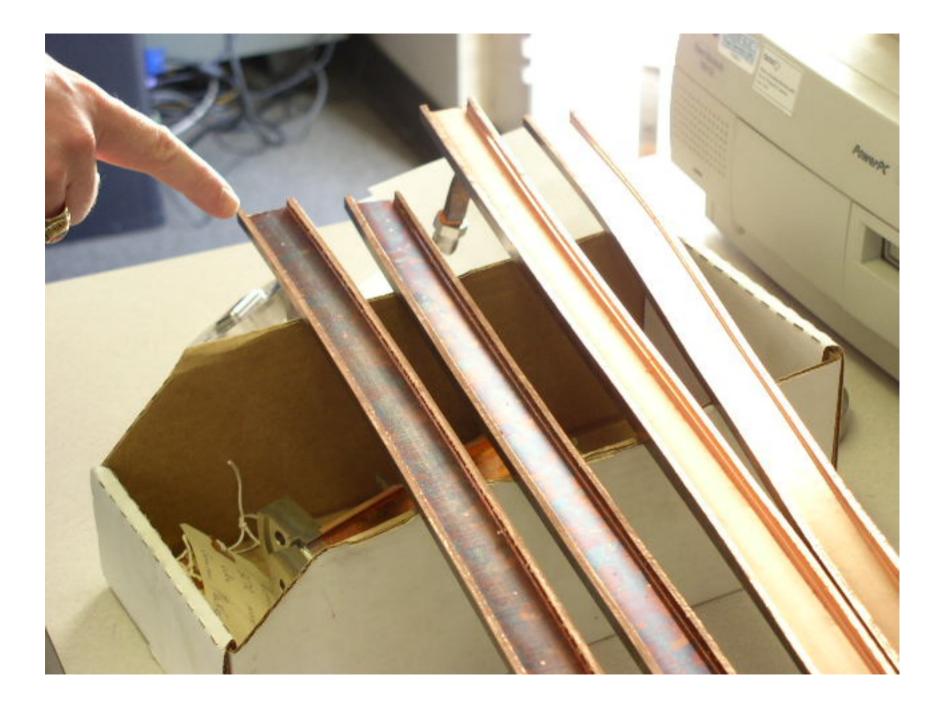
System Modifications

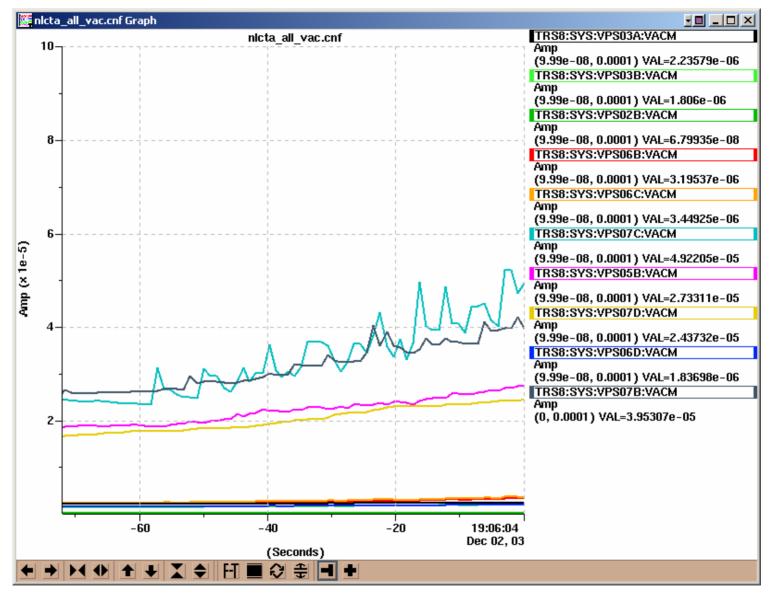
- Replaced a pumpout
- Replaced the whole line of WR90
- Cooled down the WR90 with fans
- *Hard wired* the klystrons driver together



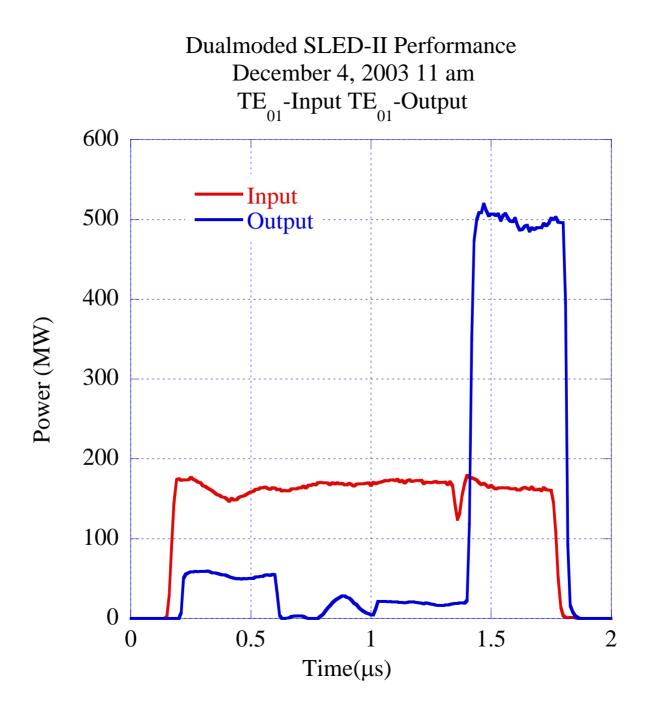


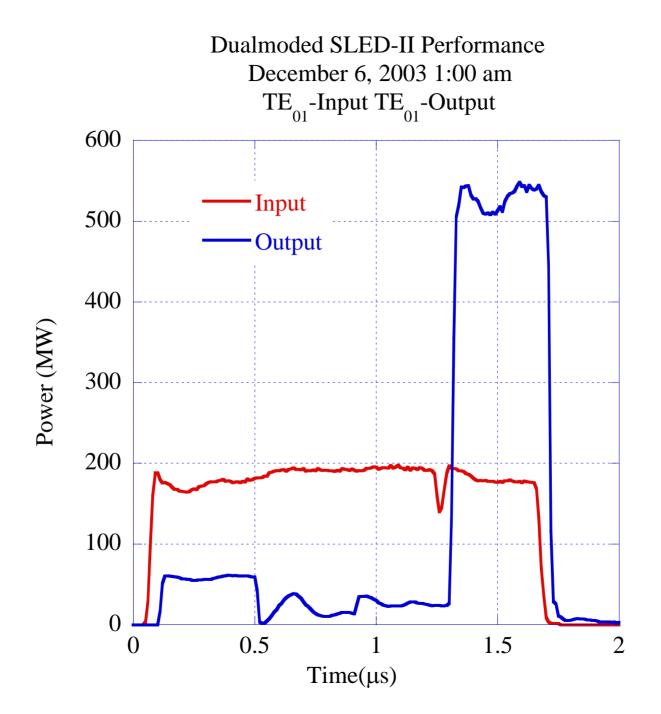


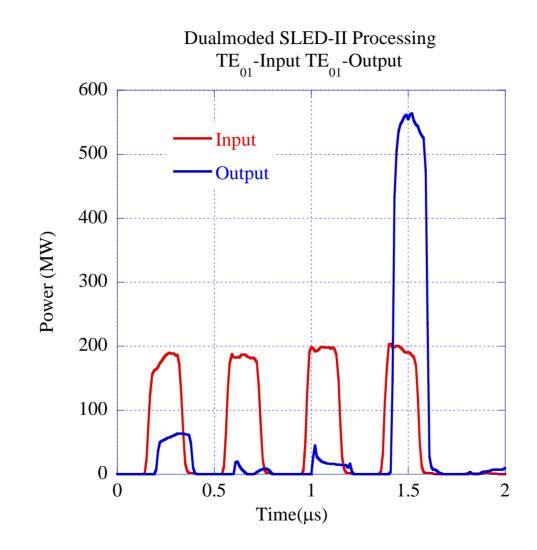


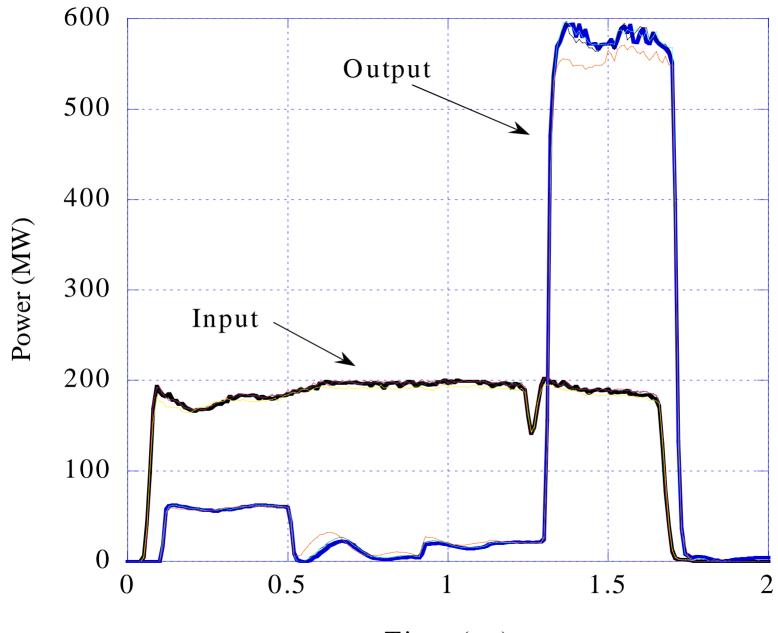


436 MW 360 ns

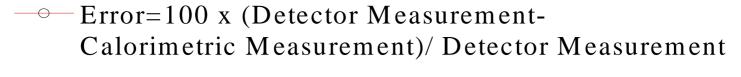


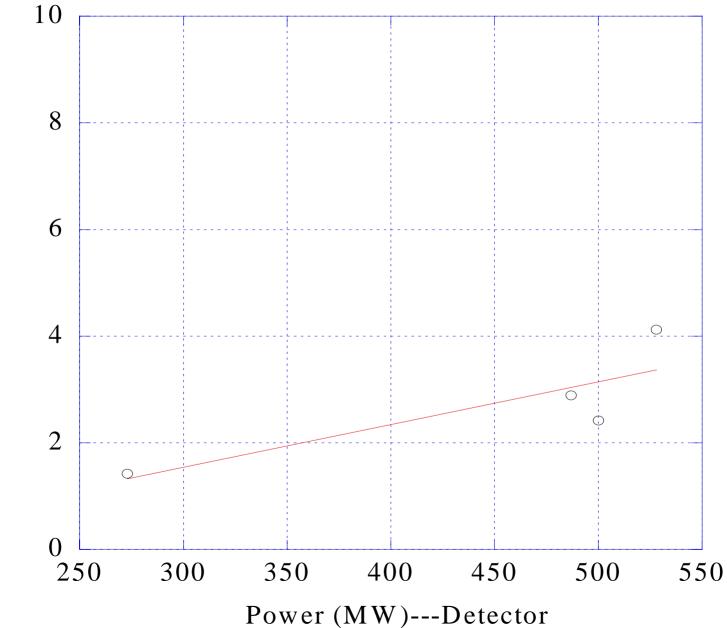




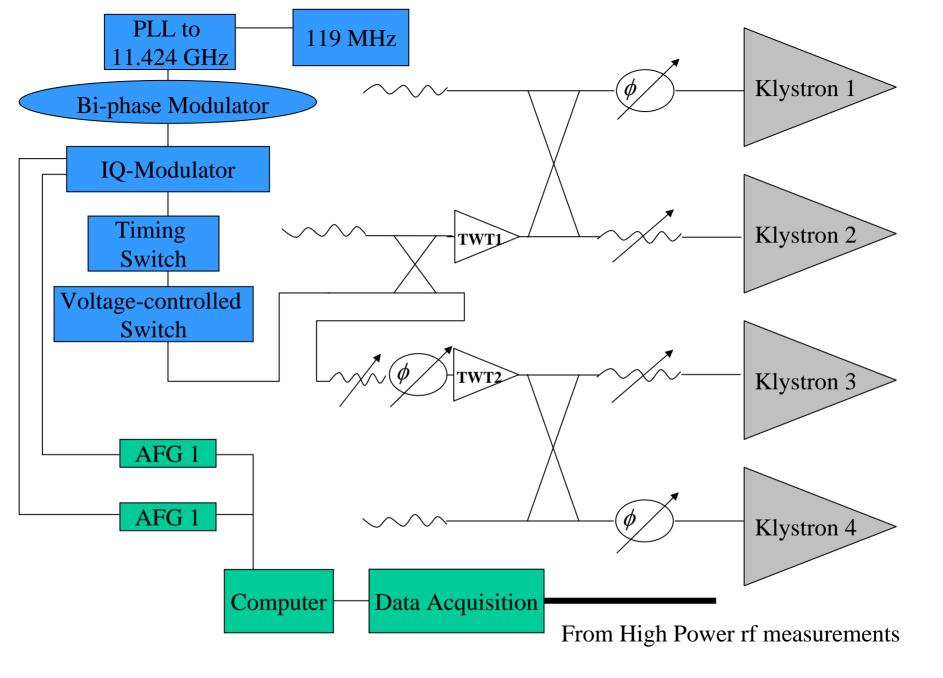


Time (µs)

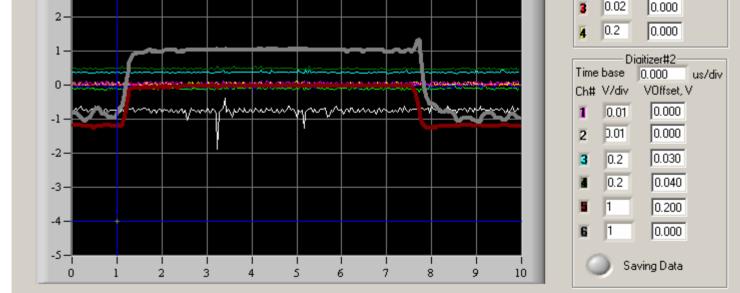


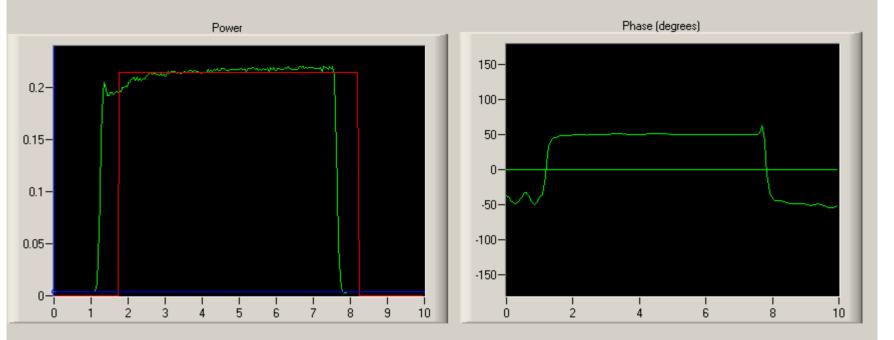


Error (%)



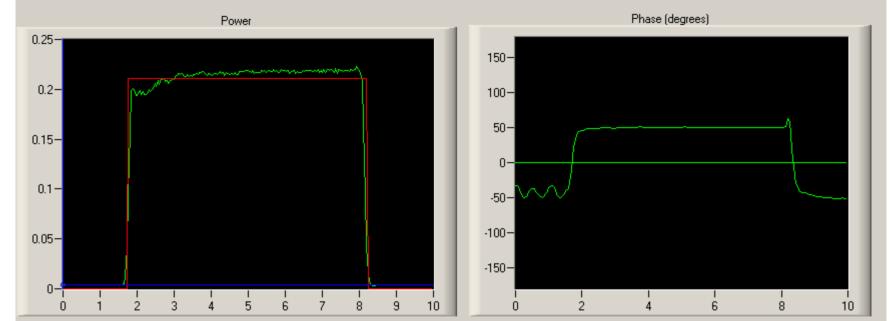
Low-Level RF Architecture



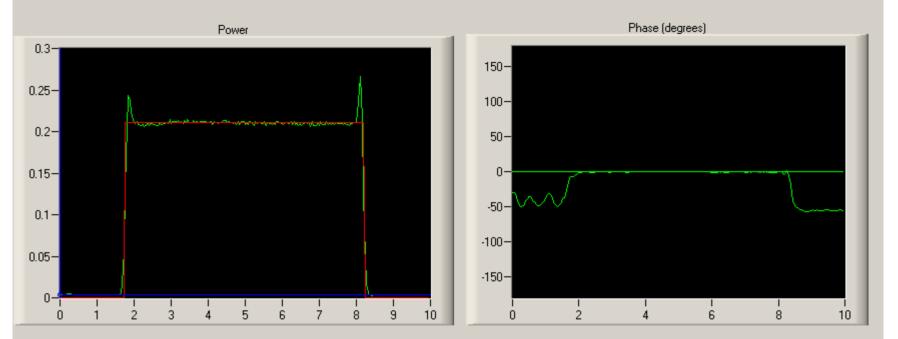


0.00394136429129

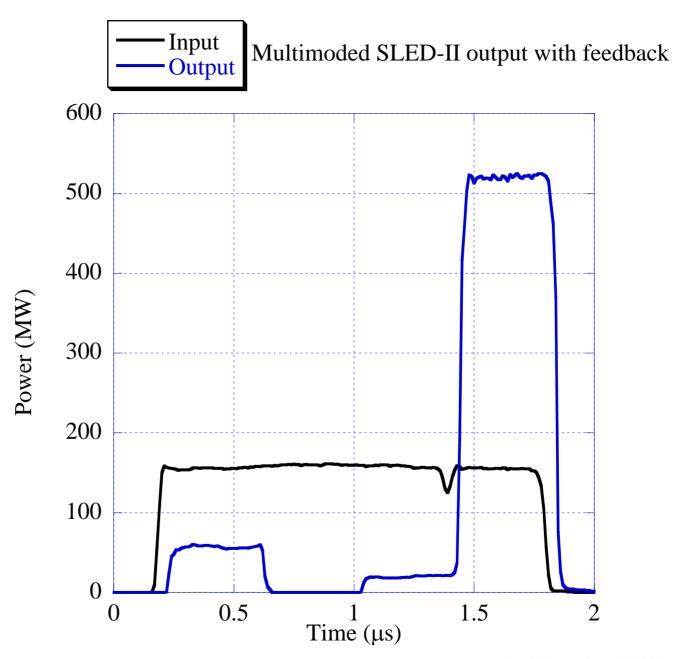






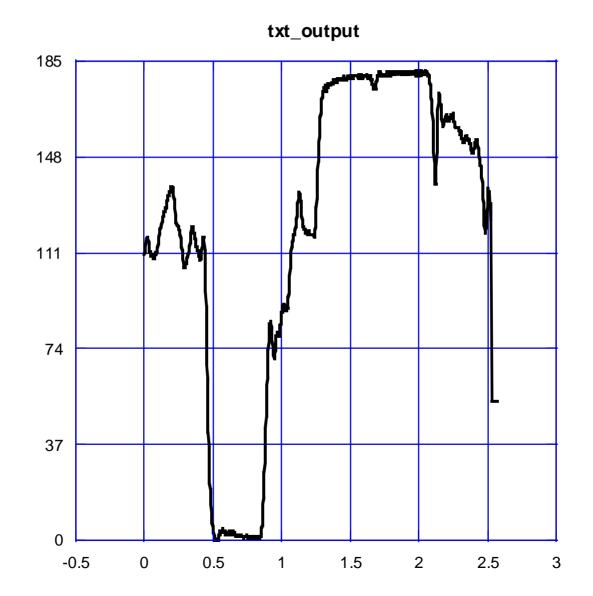


0.00379301656647

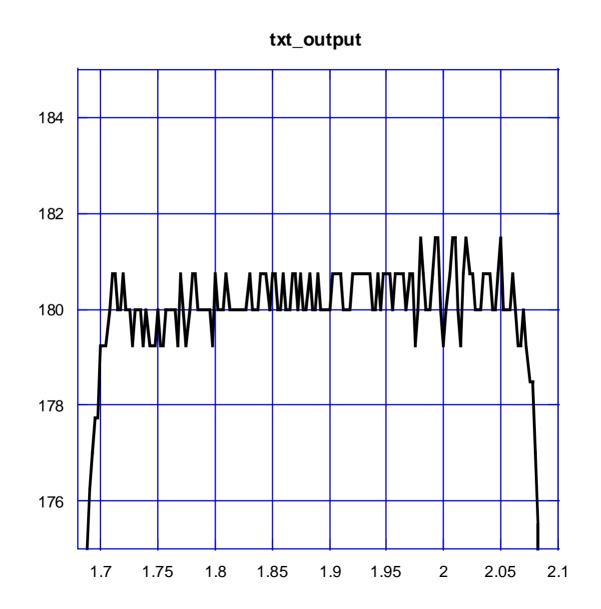


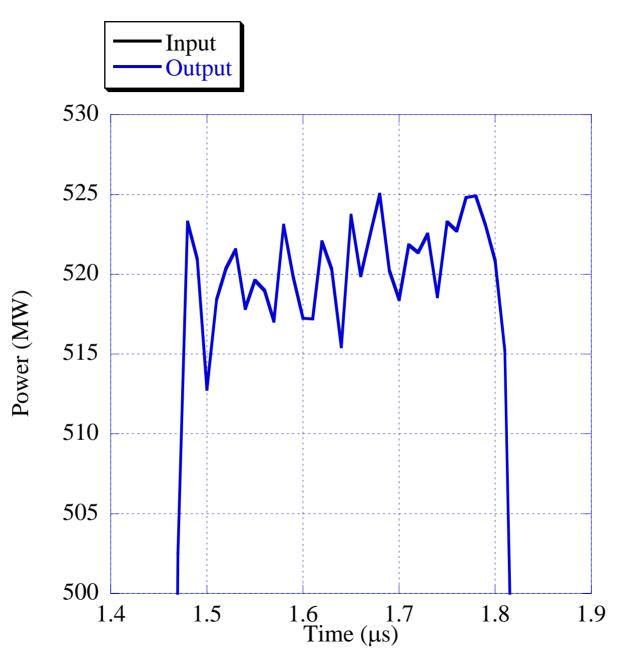
Sami Tantawi (1/27/2004)



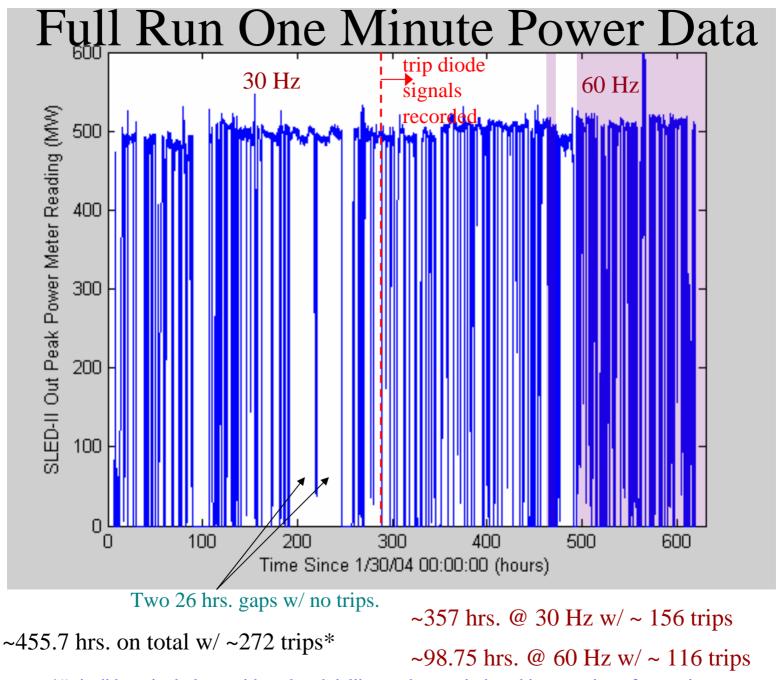


|--|



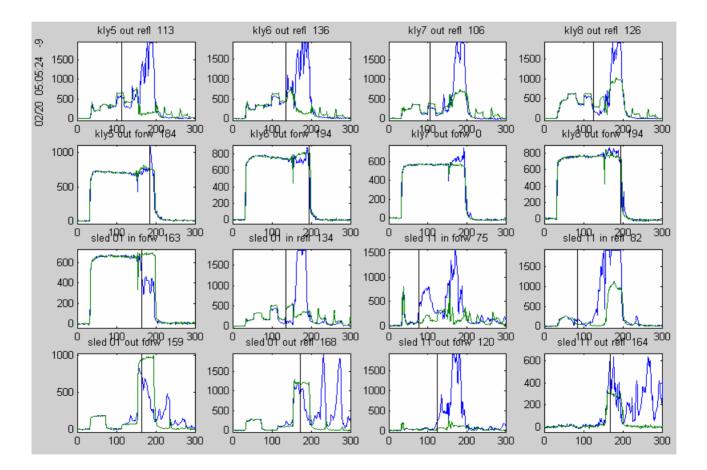


Sami Tantawi (1/27/2004)

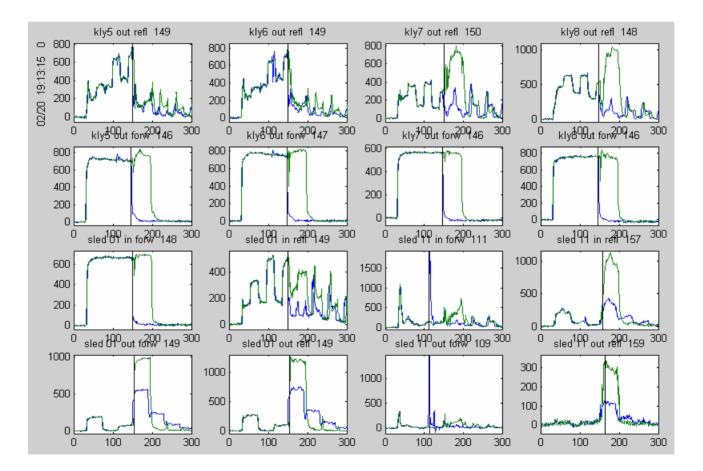


*"trips" here includes accidental and deliberate human induced interruption of operation.

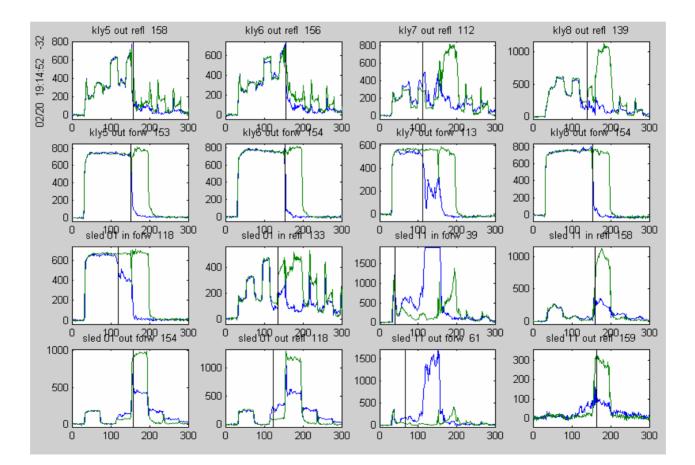
SLED Breakdown Event

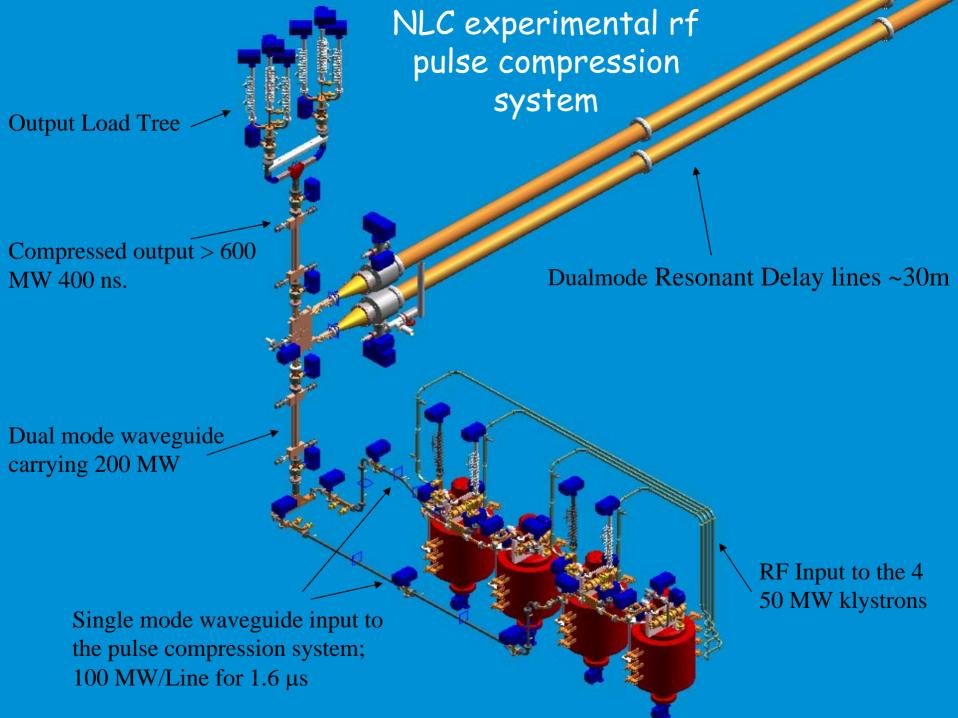


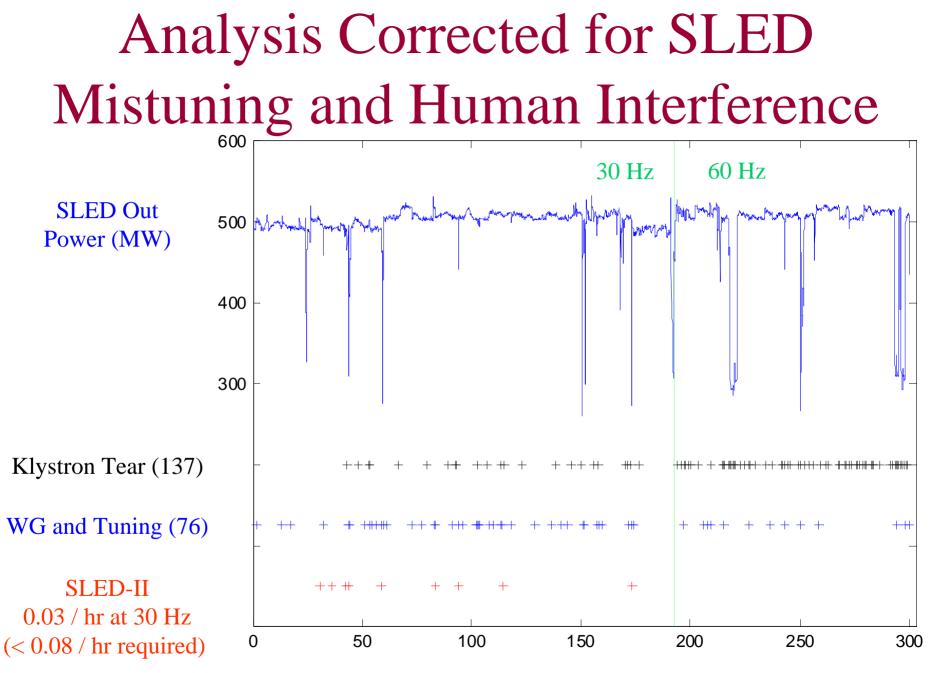
Unclear event



Klystron Breakdown event







Hours of High Power Operation

Diode Trip Analysis (Since 2/11)

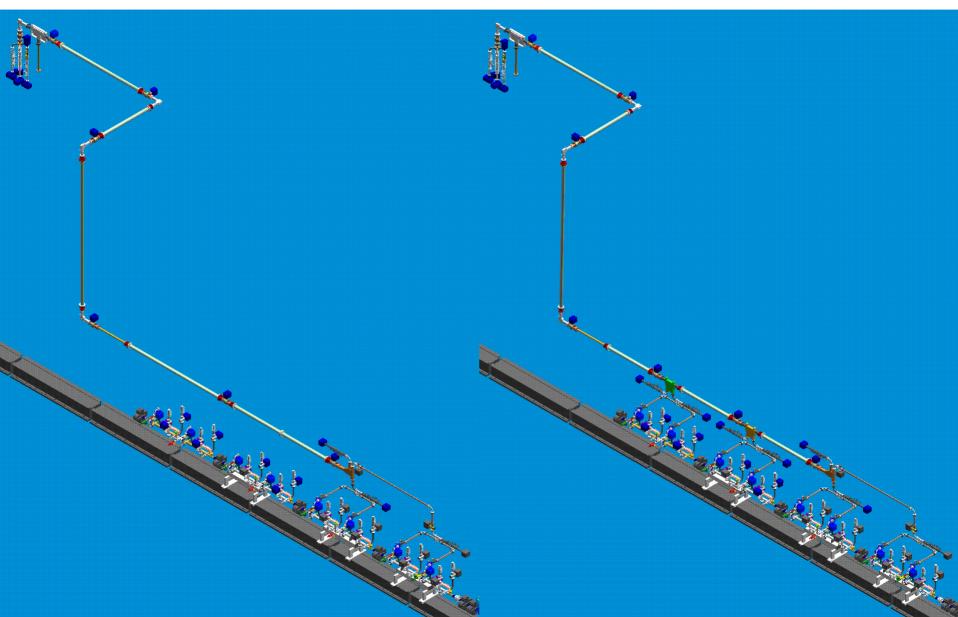
Out of 211 trips in 365.65 hrs (30Hz equivalent)

- 29 -SLED or Combiner
- 1 Klystron 5
- 15 Klystrons 5&6
- 18 Klystron 6
- 72 Klystron 7
- 28 Klystrons 7&8
- 38 Klystron 8
- 1 Loads
- 7 ?

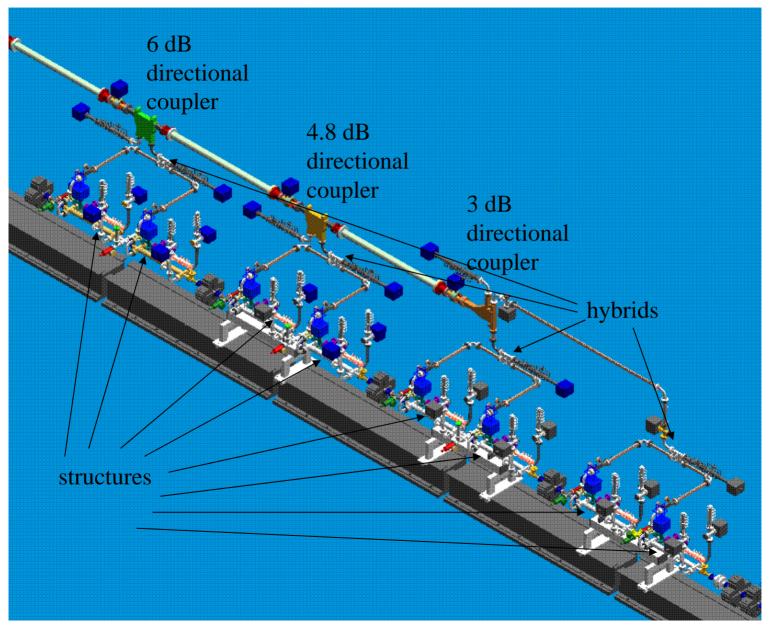
+ dig. Vac. faults

8-Pack Phase 2a

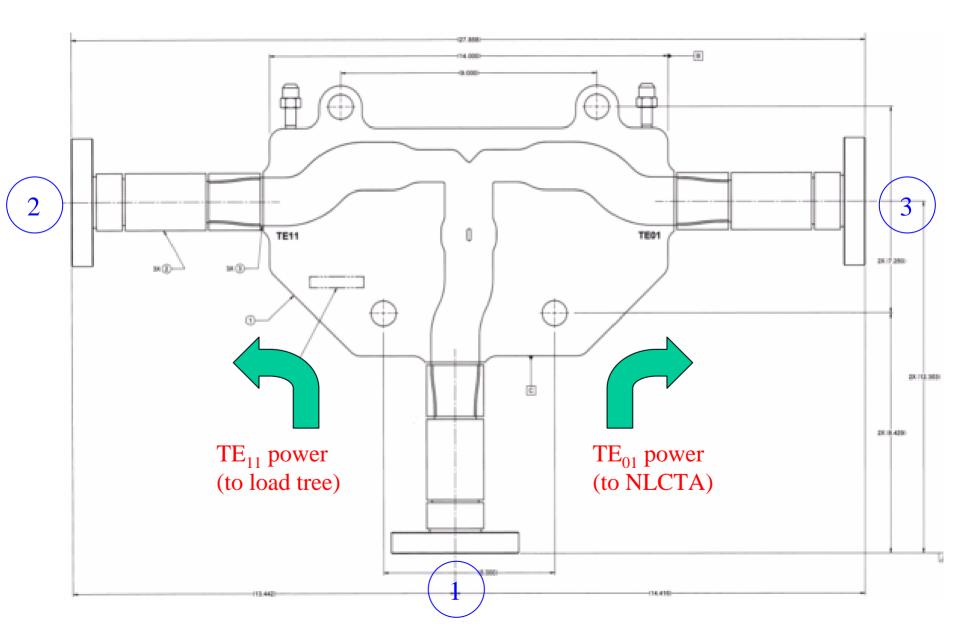
8-Pack Phase 2b



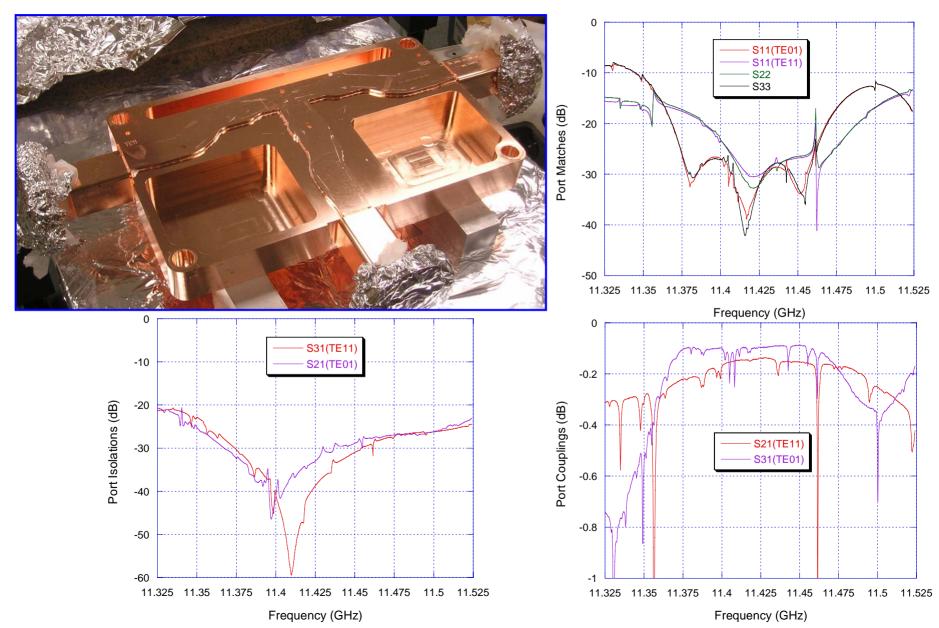
Power Distribution



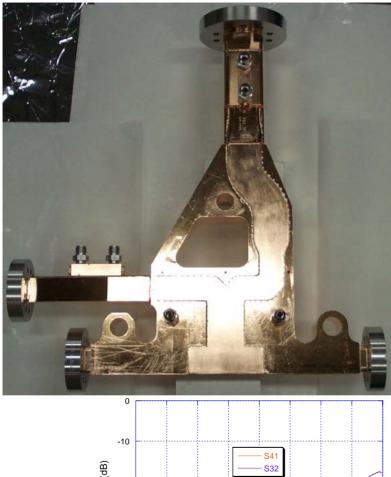
Mode Stripper

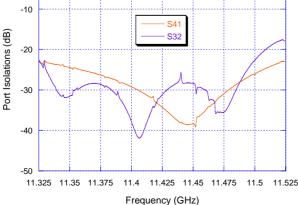


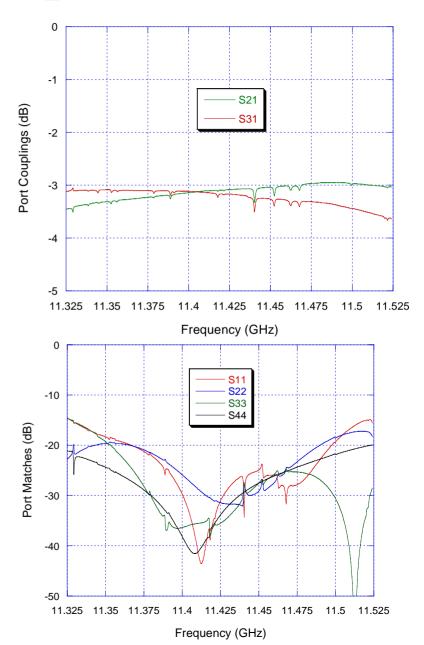
Mode Stripper Cold Test

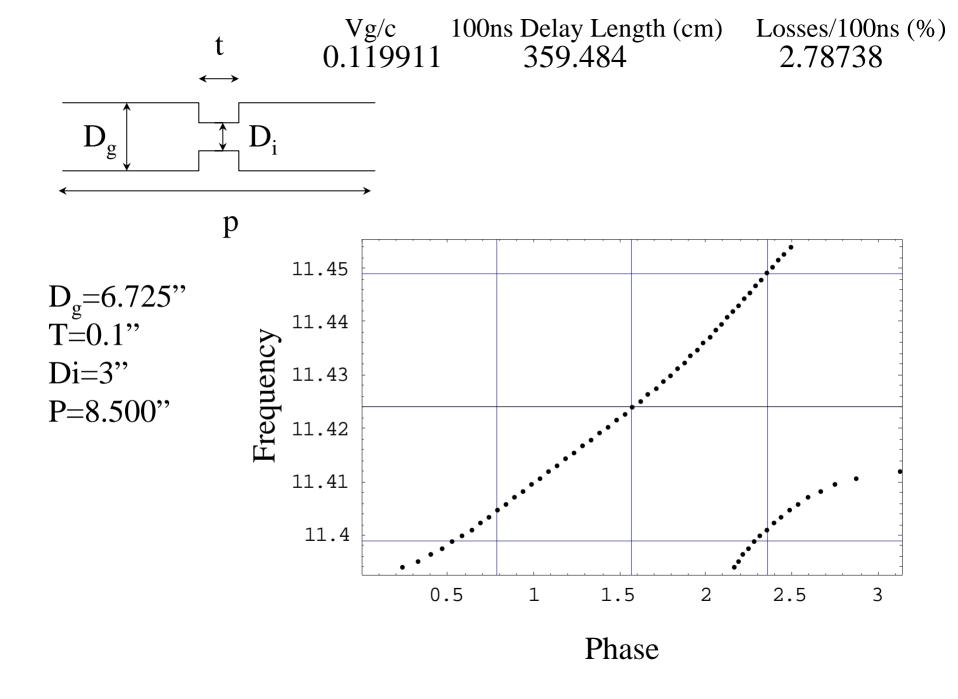


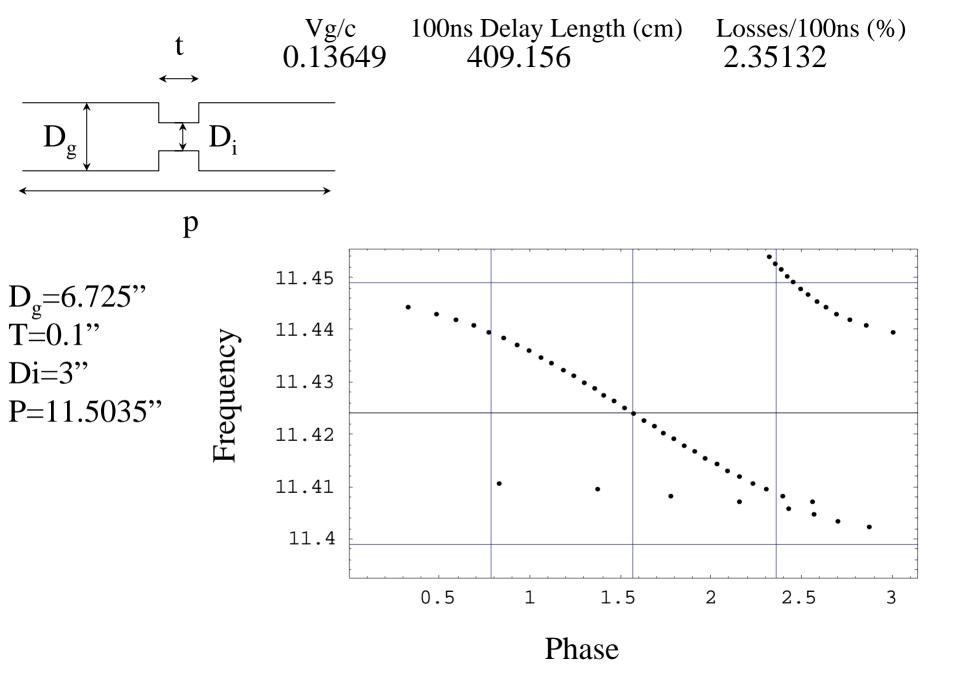
3 dB Directional Coupler Cold Test



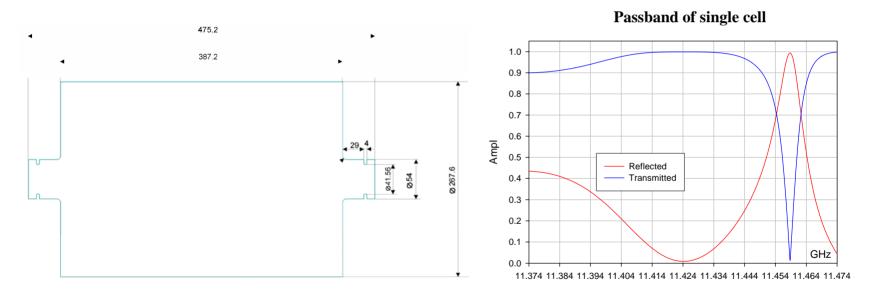




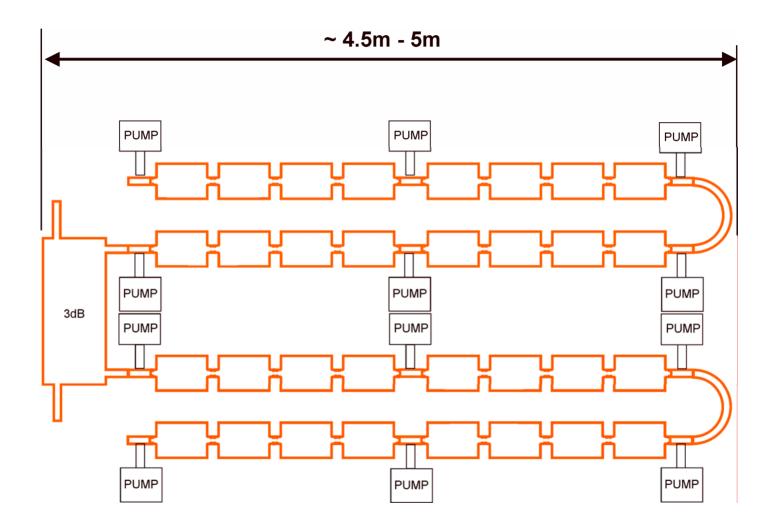


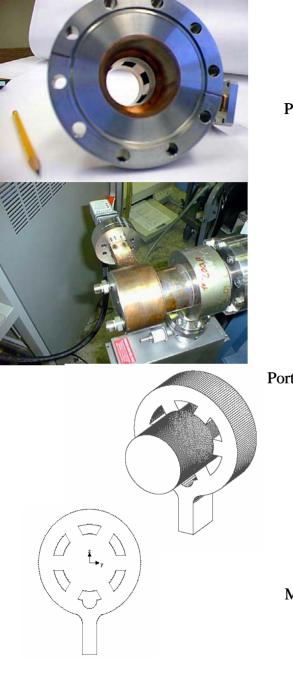


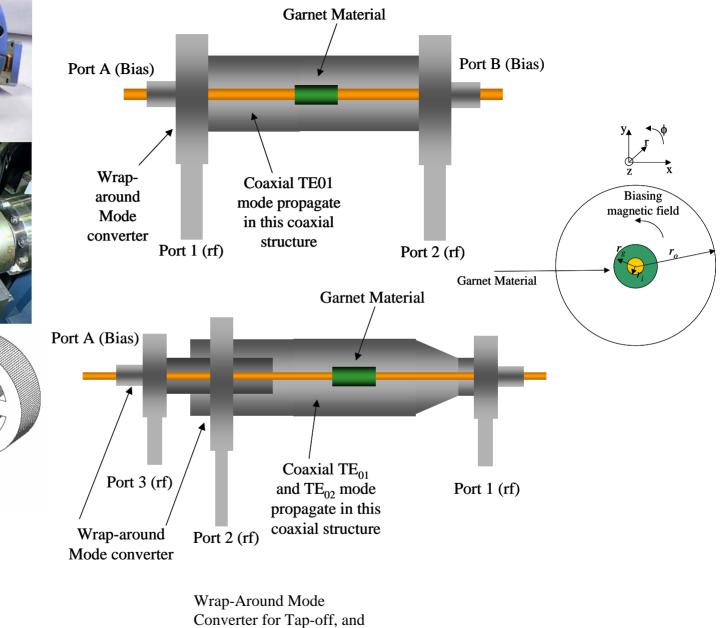
The cell is not matched enough. We can match two cell or structure of several cells by placing the cells at right distance from each to other. But to get more smooth passband is better to match each cell by iris.



Geometry of cell with matching irises.







extraction, tested to 470 MW

Conclusion:

- We have introduce a fully dual mode rf system
- We have shown design and experimental data for over moded components that propagates two modes at the same time. These component perform all possible function found in single moded rf systems
- At the operating frequency of 11.424 GHz, the peak electric field is ~49 MV/m (400 ns) and the peak Magnetic field is ~0.17 MA/m (400 ns). This was demonstrated to be low enough for a reliable high power operation of the system.

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- We have introduce a fully dual mode rf system
- We have shown design and experimental data for over moded components that propagates two modes at the same time. These component perform all possible function found in single moded rf systems
- At the operating frequency of 11.424 GHz, the peak electric field is ~49 MV/m (400 ns) and the peak Magnetic field is ~0.17 MA/m (400 ns). This should be low enough for a reliable high power operation of the system (remain to be seen)
- We have invented several new measurement techniques and instrumental components needed for characterizing dual moded rf systems.